Complexity and Approximation for the Precedence Constrained Scheduling Problem with Large Communications Delays

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Complexity and approximation for the precedence constrained scheduling problem with large communications delays

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Abstract

We investigate the problem of minimizing the makespan (resp. the sum of the completion time) for the multiprocessor scheduling problem. We show that there is no hope of finding a $\rho$-approximation with $\rho < 1 + \frac{1}{c+4}$ (resp. $1 + \frac{1}{2c+5}$) (unless $\mathcal{P} = \mathcal{NP}$) for the case where all the tasks of the precedence graph have unit execution times, where the multiprocessor is composed of an unrestricted number of machines, and where $c$ denotes the communication delay between two tasks $i$ and $j$ submitted to a precedence constraint and to be processed by two different machines. The problem becomes polynomial whenever the makespan is at the most $(c + 1)$. The case where it is $(c + 2)$ is still partially opened.

Résumé

Nous étudions le problème qui consiste à minimiser la durée totale d’un ordonnancement (resp. la somme des dates de fin d’exécution). Dans le cas où les tâches ont une durée d’exécution unitaire et où le nombre de processeurs est non borné, nous montrons qu’il n’existe pas (à moins que $\mathcal{P} = \mathcal{NP}$) de $\rho$-approximation telle que $\rho < 1 + \frac{1}{c+4}$ (resp. $1 + \frac{1}{2c+5}$) où $c$ est le délai de communication entre 2 tâches $i$ et $j$ soumises à une contrainte de précéende et devant être exécutées sur 2 processeurs différents. Le problème devient polynomial lorsque la durée d’ordonnancement est au plus $c + 1$ mais reste en partie ouvert lorsqu’il est égal à $c + 2$.

1 Introduction

Scheduling theory is concerned with the optimal allocation of scarce resources to activities over time. The theory of the design of algorithms for scheduling is younger, but still has a significant history.

In this article we adopt the classical scheduling delay model or homogeneous model in which an instance of a scheduling problem is specified by a set $J = \{j_1, \ldots, j_n\}$ of $n$ nonpreemptive tasks, a set of $U$ of $q$ precedence constraints $(j_i, j_k)$ such that $G = (J, U)$ is an directed acyclic graph (dag), the processing times $p_i, \forall j_i \in J$, and the communication times $c_{ik}, \forall (j_i, j_k) \in U$.

If the task $j_i$ starts its execution at time $t$ on processor $\pi$ and task $j_k$ is a successor of $j_i$ in the dag; then either $j_k$ starts its execution after the time $t + p_j$ on processor $\pi$ or after time $t + p_j + c_{j_i, j_k}$ on some other processor. In what follows we consider the case of $\forall j_k \in J, p_k = 1$ and $\forall (j_i, j_k) \in E, c_{j_i, j_k} = c \geq 2$.

This model was first introduced, by Rayward-Smith [15]. In this model we have a set of identical processors that are able to communicate in a uniform way. We want to use these processors in order to process a set of tasks that are subject to precedence constraints. The problem is to find a trade-off between the two extreme solutions, namely, execute all the tasks sequentially without communications, or try to use all the potential parallelism but at the cost of an increased communication overhead. This model has been extensively studied these last years both from the complexity and the (non)-approximability points of view [2].

Using the three fields notation scheme proposed by Graham et al. [6], the problem is denoted as $P|\text{prec}, c_{ij} = c \geq 2; p_i = 1|\text{C}_{\text{max}}$. 


i.e. we have an unbounded number of identical processors in order to schedule a dag such that each task has the same execution time and each pair of tasks have the same communication time. The aim is to minimize the length of the schedule.

1.1 Complexity results

The problems with unitary communication delay In the case where we consider the problem is to schedule on an unbounded number of processors a precedence graph with unitary communications delays and unit execution time (UET-UCT), Hoogeveen et al. [8] proved that the decision problem associated to $P|\text{prec}; c_{ij} = 1; p_i = 1|\text{C}_{\text{max}}$ becomes $\mathcal{NP}$-complete even for $\text{C}_{\text{max}} = 6$, and that it is polynomial for $\text{C}_{\text{max}} = 5$. Their proof is based on a reduction from the $\mathcal{NP}$-complete problem 3SAT [4]. The $\mathcal{NP}$-completeness result for $\text{C}_{\text{max}} = 6$ implies that there is no polynomial time approximation algorithm with ratio guarantee better than 7/6, unless $\mathcal{P} = \mathcal{NP}$.

Moreover, in presence of a bounded number of processors, Hoogeveen et al. [8] establish that whether an instance of $P|\text{prec}; c_{ij} = 1; p_i = 1|\text{C}_{\text{max}}$ has a schedule of length of at the most 4 is $\mathcal{NP}$-complete (they use a reduction from the $\mathcal{NP}$-complete problem Clique), whereas Picouleau [13] develops a polynomial time algorithm for the $\text{C}_{\text{max}} = 3$. In the same way, the $\mathcal{NP}$-completeness result for $\text{C}_{\text{max}} = 4$ implies that there is no polynomial time approximation algorithm with ratio guarantee better than 5/4, unless $\mathcal{P} = \mathcal{NP}$.

The problems with large communication delay In the case where we consider the problem to schedule on bounded number of processors a precedence graph with large communications delays and unit execution time (UET-LCT), Bampis et al. in [1] proved that the decision problem denoted by $P|\text{prec}; c_{ij} = c \geq 2; p_i = 1|\text{C}_{\text{max}}$ for $\text{C}_{\text{max}} = c + 3$ is $\mathcal{NP}$-complete problem, and for $\text{C}_{\text{max}} = c + 2$ (for the special case $c = 2$), they develop a polynomial time algorithm. Their proof is based on a reduction from the $\mathcal{NP}$-complete problem Balanced Bipartite Complete Graph, BBCG [4, 16]. Thus, Bampis et al. [1] proved that the $P|\text{prec}; c_{ij} = c \geq 2; p_i = 1|\text{C}_{\text{max}}$ problem does not possess a polynomial time approximation algorithm with ratio guarantee better than $(1 + \frac{1}{c+3})$, unless $\mathcal{P} = \mathcal{NP}$.

Remark: Notice that in the case where the number of processors is unbounded ($\bar{P}|\text{prec}; c_{ij} = c \geq 2; p_i = 1|\text{C}_{\text{max}}$), the complexity to an associated decision problem is unknown.

<table>
<thead>
<tr>
<th>Type of machines</th>
<th>$c_{ij}$</th>
<th>$C_{\text{max}}$</th>
<th>Complexity</th>
<th>Lower bound</th>
<th>References</th>
</tr>
</thead>
<tbody>
<tr>
<td>$\bar{P}$</td>
<td>$c = 1$</td>
<td>5</td>
<td>Polynomial</td>
<td>$7/6 \leq \rho$</td>
<td>[17]</td>
</tr>
<tr>
<td>$\bar{P}$</td>
<td>$c = 1$</td>
<td>6</td>
<td>$\mathcal{NP}$-complete</td>
<td>$7/6 \leq \rho$</td>
<td>[17]</td>
</tr>
<tr>
<td>$\bar{P}$</td>
<td>$c = 1$</td>
<td>3</td>
<td>Polynomial</td>
<td></td>
<td>[13]</td>
</tr>
<tr>
<td>$\bar{P}$</td>
<td>$c = 1$</td>
<td>4</td>
<td>$\mathcal{NP}$-complete</td>
<td>$5/4 \leq \rho$</td>
<td>[17]</td>
</tr>
<tr>
<td>$\bar{P}$</td>
<td>$c \geq 2$</td>
<td>&gt; $c$</td>
<td>?</td>
<td>?</td>
<td>?</td>
</tr>
<tr>
<td>$\bar{P}$</td>
<td>$c \geq 2$</td>
<td>$c + 1$</td>
<td>Polynomial</td>
<td></td>
<td>[1]</td>
</tr>
<tr>
<td>$\bar{P}$</td>
<td>$c \geq 2$</td>
<td>$c + 3$</td>
<td>$\mathcal{NP}$-complete</td>
<td>$1 + 1/(c + 3) \leq \rho$</td>
<td>[1]</td>
</tr>
</tbody>
</table>

Table 1. Complexity results for scheduling problems

The complexity results are given by the summary table 1.

1 $3\text{SAT}$ see [4]
2 $\text{Clique}$ see [4]
3 $\text{BBCG}$ see [4, 16]
1.2 Approximation results

The problems with unitary communication delay The best known approximation algorithm for \( P|\text{prec}; c_{ij} = 1; p_i = 1|C_{\text{max}} \) is due to Munier and König [12]. They presented a \((4/3)\)-approximation algorithm for this problem, which is based on an integer linear programming formulation. They relax the integrity constraints and they produce a feasible schedule by rounding.

Munier and Hanen [11] proposed a \((\frac{7}{6} - \frac{1}{3m})\)-approximation algorithm for the problem \( P|\text{prec}; c_{ij} = 1; p_i = 1|C_{\text{max}} \). They define and study a new list scheduling approximation algorithm based on the solution given on an unrestricted number of processors. They introduce the notion of favorite successor in order to define priorities between conflicting successors of a task. Notice that, in the case where we consider the large communications delays, there is no polynomial time, as known, approximation algorithm except the trivial bound \((c + 1)\) whose first step consists in executing the tasks and second step in initiating communications phasis and so on ...

Concerning the case of the number where processors is restricted, an only (as known) constant 2-approximation algorithm is given by Munier [9], for the special case where the precedence graph is tree in presence of large communications delays.

The problems with large communication delay Contrary to the complexity results, as we know, an unique approximation algorithm is given by Rapine [14]. The autor gives the lower bound \( O(c) \) for the list scheduling in presence of large communications delays. Approximation results are given by the table 2.

<table>
<thead>
<tr>
<th>Type of machines</th>
<th>( c_{ij} )</th>
<th>Approximation results</th>
<th>References</th>
</tr>
</thead>
<tbody>
<tr>
<td>( P )</td>
<td>( c = 1 )</td>
<td>( \rho \leq 4/3 )</td>
<td>[11]</td>
</tr>
<tr>
<td>( P )</td>
<td>( c = 1 )</td>
<td>( \rho \leq 7/3 )</td>
<td>[10]</td>
</tr>
<tr>
<td>( P )</td>
<td>( c \geq 2 )</td>
<td>2 for tree</td>
<td>[9]</td>
</tr>
<tr>
<td>( P )</td>
<td>( c \geq 2 )</td>
<td>?</td>
<td>?</td>
</tr>
<tr>
<td>( P )</td>
<td>( c \geq 2 )</td>
<td>( O(c) )</td>
<td>[14]</td>
</tr>
</tbody>
</table>

Table 2. Approximation results for scheduling problems

1.3 Presentation of an article

The challenge is to determinate a threshold for approximation algorithm for the problem \( P|\text{prec}; c_{ij} = c \geq 2; p_i = 1|C_{\text{max}} \), to develop a non trivial approximation algorithm, and to improve, in the presence of a restricted number of processors, the bound given by Rapine [14].

This article is organized as follows: in the second section, we give a preliminary result. In the third section, we give the two non-approximabilities results for the scheduling problem with the objective function of minimizing the length of the schedule (resp. minimization of the completion time). In the fourth section, we propose a polynomial time algorithm for \( C_{\text{max}} = c + 2 \) with \( c \in \{2, 3\} \). In the last section, we develop a \( \frac{2(c+1)}{3} \)-approximation algorithm based on the notion of expansion, of the makespan, of a good feasible schedule.

2 Preliminary result

In this part, we will define a variant of SAT problem [4], denoted in what follows by \( P_1 \). The \( \mathcal{NP} \)-completeness of the scheduling problem \( P|\text{prec}; c_{ij} = c \geq 2; p_i = 1|C_{\text{max}} \) (see section 3), is based on a reduction from this problem.
The problem \(P_1\) is a variant of the well known SAT problem [4]. We will call this variant the One-in-(2, 3)SAT(2, 1) problem. We denote by \(V\), the set of variables. Let \(n\) be a multiple of 3 and let \(C\) be a set of clauses of cardinality 2 or 3. There are \(n\) clauses of cardinality 2 and \(n/3\) clauses of cardinality 3 so that:

- each clause of cardinality 2 is equal to \((x \lor y)\) for some \(x, y \in V\) with \(x \neq y\).
- each of the \(n\) literals \(x\) (resp. of the literals \(\bar{x}\)) for \(x \in V\) belongs to one of the \(n\) clauses of cardinality 2, thus to only one of them.
- each of the \(n\) literals \(x\) belongs to one of the \(n/3\) clauses of cardinality 3, thus to only one of them.
- whenever \((x \lor y)\) is a clause of cardinality 2 for some \(x, y \in V\), then \(x\) and \(y\) belong to different clauses of cardinality 3.

**Question:**
Is there a truth assignment \(I : V \rightarrow \{0, 1\}\) such that every clause in \(C\) has exactly a true literal?

In order to illustrate \(P_1\), we consider the following example.

**Example**
The following logic formula is a valid instance of \(P_1\):

\[
(x_0 \lor x_1 \lor x_2) \land (x_3 \lor x_4 \lor x_5) \land (\bar{x}_0 \lor x_3) \land (\bar{x}_3 \lor x_0) \land (\bar{x}_4 \lor x_2) \land (\bar{x}_1 \lor x_4) \land (\bar{x}_5 \lor x_1) \land (\bar{x}_2 \lor x_5).
\]

The answer to \(P_1\) is yes. It suffices to choose \(x_0 = 1, x_3 = 1\) and \(x_i = 0\) for \(i = \{1, 2, 4, 5\}\). This yields a truth assignment satisfying the formula, and there is exactly one true literal in every clause. For the proof of the \(\mathcal{NP}\)-completeness see [5].

3 Non-approximability results

In this section, we show in the first part, that the problem denoted by \(\bar{P}_{\text{prec}}; c_{ij} = c \geq 3; p_i = 1|C_{\text{max}}\) does not possess a polynomial time approximation algorithm with ratio guarantee better than \(1 + \frac{1}{c+4}\) for the minimization of the length of the schedule (resp. \(1 + \frac{1}{2c+6}\), for the minimization of the sum of the completion time). We also give the \(\mathcal{NP}\)-completeness for the special case \(c = 2\) and \(C_{\text{max}} = 6\) in the section 3.2.

3.1 The minimization of length of the schedule

![Diagram](image)

**Fig. 1.** A partial precedence graph for the \(\mathcal{NP}\)-completeness of the scheduling problem \(\bar{P}_{\text{prec}}; c_{ij} = c \geq 3; p_i = 1|C_{\text{max}}\).

**Remark:** \(\bar{p}\) is in the clause of length two associated to \(D_1' \rightarrow D_2' \rightarrow \ldots \rightarrow D_{c+2}' \rightarrow D_{c+3}'\)
Theorem 1. The problem of deciding whether an instance of $P|\text{prec}; c_{ij} = c; p_i = 1|C_{\text{max}}$ has a schedule of length at most $(c + 4)$ is $NP$-complete with $c \geq 3$.

Proof. It is easy to see that $P|\text{prec}; c_{ij} = c; p_i = 1|C_{\text{max}} = c + 4 \in NP$.

Our proof is based on a reduction from $P_1$.

Given an instance $\pi^*$ of $P_1$, we construct an instance $\pi$ of the problem $P|\text{prec}; c_{ij} = c; p_i = 1|C_{\text{max}} = c+4$, in the following way:

1. For all $x \in V$, we introduce $(c + 6)$ variables-tasks: $\alpha_{x'; x}, \alpha_{x''}, x', x'', \beta_{j'}$ with $j \in \{1, 2, \ldots, c + 2\}$. We add the precedence constraints: $\alpha_{x'; x'} \rightarrow x', \alpha_{x'; x''} \rightarrow x'', \beta_{j'} \rightarrow x', \beta_{j'} \rightarrow x''$ with $j \in \{1, 2, \ldots, c + 1\}$.\[\]
2. For all clauses of length three denoted by $C_i = (y \lor z \lor t)$, we introduce $2 \times (2 + c)$ clauses-tasks $C_i^j$ and $A_i^j, j \in \{1, 2, \ldots, c + 2\}$, with precedence constraints: $C_i^j \rightarrow C_i^{j+1}$ and $A_i^j \rightarrow A_i^{j+1}, j \in \{1, 2, \ldots, c + 1\}$.

We add the constraints $C_i^j \rightarrow l$ with $l \in \{y', z', t'\}$ and $l \rightarrow A_i^{j+2}$ with $l \in \{y', z', t'\}$.

3. For all clauses of length two denoted by $C_i = (x \lor y)$, we introduce $(c + 3)$ clauses-tasks $D_i^j, j \in \{1, 2, \ldots, c + 3\}$ with precedence constraints: $D_i^j \rightarrow D_i^{j+1}$ with $j \in \{1, 2, \ldots, c + 2\}$ and $l \rightarrow D_i^{c+3}$ with $l \in \{x', y'\}$.

The above construction is illustrated in Figure 1. This transformation can be clearly computed in polynomial time.

- Let us first assume that there is a schedule of length at most $(c + 4)$. In the following, we will prove that there is a truth assignment $I : V \rightarrow \{0, 1\}$ such that each clause in $C$ has exactly one true literal.

First we can remark that if $c \geq 3$ then $2c + 2 > c + 4$ and so, each path $A_i^j, \beta_i^j, C_i$ or $D_i^j$, with $j \in \{1, 2, \ldots, c + 2\}$ and $j' \in \{1, 2, \ldots, c + 3\}$ must be executed on the same processor. What’s more, two of these paths cannot be executed on the same processor.

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Notation: In the following we denote by $P_A$ (resp. $P_D$) the set of the processors which execute a path $A_i^j$ (resp. a path $C_i^j$). Notice that we know by the definition of the problem $P_1$, that in an instance admits no clauses of length three where $n$ denotes the number of variables. In the same way, we denote by $P_B$ (resp. $P_D$) the set of the processors which execute a path $D_i^j$ (resp. a path $D_i^j$).

Lemma 1. If $C_{\text{max}} = c + 4$ then assigning the value true to the variable $x$ if and only if the variable-task $x'$ is executed on a processor of the path $P_C$ we obtain a correct solution.

Proof. In order to respect the feasible schedule of length $(c + 4)$, in the first time, we can stem from the polynomial time transformation, that the starting time of the variables-tasks $l'$, $l''$ and $l''$, and that the processors on which these tasks must be executed, are given by the following remarks:

- Each variable-task $l'$ is executed on a processor of $P_C$ at slot 3 or on a processor of $P_D$ at slot $(c + 2)$ or $(c + 3)$.

- Each variable-task $l''$ is executed on a processor of $P_B$ at slot 3 or on a processor of $P_D$ at slot $(c + 2)$ or $(c + 3)$.

- Each variable-task $l'''$ is executed on a processor of $P_B$ at slot 3 or on a processor of $P_A$ at slot $(c + 2)$ or $(c + 3)$.

- The variables-tasks $l'$ and $l''$ cannot be executed together on a processor of $P_B$ (they have a common predecessor).

Notation and property: For each $l \in V$, we can associate the three tasks $l', l'', l'''$. We denote by $X = \{l'| l \in V\}$, $X = \{l''| l \in V\}$ and $X = \{l'''| l \in V\}$ three sets of tasks. For each subset $A$ of $X$ (resp. $X$), we can associate a subset $B$ of $X$ in the following way: $l' \in B$ if and only if $l'' \in A$ (resp. $l''' \in A$).

Let be the following sets:

- $X_1 = \{l'| \pi(l') = \pi(P_C)\}$ where $\pi(l')$ (resp. $\pi(P_C)$) designs the processor on which the task $l'$ is scheduled.

- $X_2 = \{l''| \pi(l'') = \pi(P_D)\}$.
\[
X_3 = \{ l' \mid \pi(l') = \pi(P_3) \},
X_4 = \{ l' \mid \pi(l') = \pi(P_D) \},
X_5 = \{ l' \mid \pi(l') = \pi(P_5) \},
X_6 = \{ l' \mid \pi(l') = \pi(P_4) \}.
\]

Let be \( x_i = |X_i| \) for \( i \in \{1, \ldots, 6 \} \).

We can stem from the construction of an instance of the scheduling problem the following table,

<table>
<thead>
<tr>
<th>( P_C )</th>
<th>( P_A )</th>
<th>( P_3 )</th>
<th>( P_4 )</th>
</tr>
</thead>
<tbody>
<tr>
<td>( x' )</td>
<td>( X_1 )</td>
<td>( X_2 )</td>
<td></td>
</tr>
<tr>
<td>( x' )</td>
<td>( X_3 )</td>
<td>( X_4 )</td>
<td></td>
</tr>
<tr>
<td>( x' )</td>
<td>( X_5 ) ( X_6 )</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

From the previous table, using the variable \( x_i \), we obtain the following inequations system:

\[
x_1 + x_2 = n \quad (1)
x_3 + x_4 = n \quad (2)
x_5 + x_6 = n \quad (3)
x_1 \leq \frac{n}{3} \quad (4)
x_6 \leq \frac{2n}{3} \quad (5)
x_3 + x_5 \leq n \quad (6)
x_2 + x_4 \leq n \quad (7)
\]

We will give some details about the previous system:

- For the equations \((1), (2)\) and \((3)\): We must execute all the tasks of the sets \( X, \bar{X} \) and \( \bar{\bar{X}} \).
- For the equation \((4)\), on the processor which executes the path \( C_i \) of the clause \( C_i = (y \lor z \lor t) \), we can execute at most one of the three variables-tasks \( y', z', t' \). Indeed, all variables-tasks \( l' \) as a successor which is executed on a processor of \( P_D \). If it is executed on the processor which scheduled the tasks from the path \( P_C \) it cannot be executed before the slot \( 3 \) and so, the variable-task \( \alpha_{l'} \) must be executed on the same processor which becomes saturated. So, we have \( |X_3| < |P_C| \).
- For the equation \((5)\), each processor of the paths \( P_A \) has two free slots and \( |P_A| = \frac{n}{3} \).
- For the equation \((6)\), all the variables-tasks \( l' \) or \( l'' \) which are executed on a processor of the path \( P_3 \) must be finished before slot \( 3 \) (it has a successor executed on another processor). So the variable-task \( \alpha_{l''} \) must be executed on the same processor which becomes saturated. Therefore, at the most one task between the variables-tasks \( l' \) and \( l'' \) can be executed on a processor of the path \( P_3 \) and so, \( |X_3| + |X_5| \leq |P_3| \).
- For the equation \((7)\), it is clear that, \( |P_D| = n \) and there is at the most one free slot on each processor of \( P_D \).

In the one hand, we have \( x_3 + x_5 = n \) and in the second hand, \( \forall l' \) only one variable-task between the variables-tasks \( l' \) and \( l'' \) can be executed on a processor of \( P_D \), thus we obtain \( X_3 \cap X_5 = \emptyset \). Consequently, we have \( X_3 \cup X_5 = X \). As the set \( X_4 \) (resp. \( X_6 \)) is the complementary of the set \( X_3 \) (resp. \( X_5 \)) we have \( X_4 \cup X_6 = X \). In more if the variable-task \( l' \) is executed on a processor of \( P_C \) then the variable-task \( \alpha_{l''} \) is executed on the same processor. The variable-task \( x' \) cannot be executed before the slot \( c + 2 \) therefore on a processor of \( P_D \). We can deduce that \( X_1 = X_4 \) (the two sets are the same cardinality). Finally, we have \( X_1 \cup X_2 = X, X_3 \cup X_4 = X, X_5 \cup X_6 = X, X_4 \cup X_6 = X, X_3 \cup X_5 = X, X_4 = X_4 \) and therefore \( X_1 = X_4 = X_5 \) and \( X_2 = X_3 = X_5 \).

We can deduce from the previous equations that \( x_1 = x_4 = x_5 = \frac{n}{3} \) and \( x_2 = x_3 = x_6 = \frac{2n}{3} \).

This concludes the proof of Lemma 1.
So, if we affect the value "true" to the variable \( l \) and only the variable-task \( l' \) is executed on a processor of \( P_c \) it is trivial to see that in the clause of length 3 we have one and only one literal equal to "true".

Let be \( c = (x \lor \bar{y}) \), a clause of length 2.

- If \( x' \in X_1 \implies y' \in X_1 \implies y' \in X_1 \). The first implication (resp. the second) is due to the fact that each processor of the path \( P_D \) must be saturated \( (x_2 + x_4 = n) \) (resp. \( X_1 = X_3 \)). Only the literal \( x \) is "true" between the variables \( x \) and \( \bar{y} \).
- If \( x' \in X_2 \implies y' \in X_3 \implies y' \in X_2 \). The first (resp. the second) implication is due to the fact that there is only one free slot on each processor executing the path \( P_D \) (resp. \( X_3 = X_2 \)). Only the literal \( \bar{y} \) is "true" between the variables \( x \) and \( \bar{y} \).

In conclusion, there is only one true literal per clause.

- Conversely, we suppose that there is a truth assignment \( I : \mathcal{V} \rightarrow \{0, 1\} \), such that each clause in \( \mathcal{C} \) has exactly one true literal.

Suppose that the true literal in the clause \( C_i = (y \lor z \lor v) \) is \( t \). Therefore, the variable-task \( t' \) (resp. \( y' \) and \( z' \)) is processed at the slot 3 (resp. at the slot \( (c + 2) \)) on the same processor as the path \( P_{c_i} \) (resp. as the path \( P_D \) and \( P_{D'} \), where \( D \) and \( D' \) indicates a clause of length two where the variables \( y \) and \( z \) occurred). The \( 2^2 \) other variables-tasks \( y' \) not yet scheduled are executed at slot 3 on processor \( P_{c_3} \) as the variable-task \( \alpha_{y'y'} \). The variable-task \( t' \) (resp. \( y' \) and \( z' \)) is executed at the slot 2 (resp. \( c + 2 \) and \( c + 3 \)) on a processor of the path \( P_{c_3} \) (resp. \( P_{c_3} \)).

This concludes the proof of Theorem 1.

### 3.2 The special case \( c = 2 \)

In this part, we will consider the special case: \( \forall(i, j) \in E, \) if \( \pi^i = \pi^j \) then \( t_j \geq t_i + p_i \) else \( t_j \geq t_i + p_i + 2 \). We study the complexity of this problem, and we will prove the \( \mathcal{NP} \)-completeness of the \( \mathcal{P}_{\text{prec}}[c_{ij} = 2; p_i = 1|C_{\text{max}} \text{ for } C_{\text{max}} \text{. Recall that (see } [13],[17]), \text{for the classical UET-UCT problem (} \mathcal{P}_{\text{prec}}[c_{ij} = 1; p_i = 1|C_{\text{max}} \text{) that the problem becomes \mathcal{NP}-complete (resp. polynomial) for } C_{\text{max}} = 6 \text{ (resp. } C_{\text{max}} = 5 \text{).}

The idea of the construction of an instance of the scheduling problem \( \mathcal{P}_{\text{prec}}[c_{ij} = 2; p_i = 1|C_{\text{max}} \text{ is similar as previously, and it is based on the problem } \Pi_1 \).

**Theorem 2.** The problem of deciding whether an instance of \( \mathcal{P}_{\text{prec}}[c_{ij} = 2; p_i = 1|C_{\text{max}} = 6 \text{ has a schedule of length at most six is } \mathcal{NP}-complete.

**Proof.** It is easy to see that \( \mathcal{P}_{\text{prec}}[c_{ij} = 2; p_i = 1|C_{\text{max}} = 6 \in \mathcal{NP}. \)

Our proof is based on a reduction from \( \Pi_1 \).

Given an instance \( \pi^* \) of \( \Pi_1 \), we construct an instance \( \pi \) of the problem \( \mathcal{P}_{\text{prec}}[c_{ij} = 2; p_i = 1|C_{\text{max}} = 6 \), in the following way:

1. For all \( x \in \mathcal{V} \), we introduce 5 variables-tasks: \( \alpha_x, \alpha_x', x', \bar{x}', \beta_{x'}, \beta_{x'} \). We add the precedence constraints: \( \alpha_x \rightarrow x', \alpha_x' \rightarrow \bar{x}', \beta_{x'} \rightarrow \bar{x}' \rightarrow \bar{x}'. \)
2. For all clauses of length three denoted by \( C_i = (y \lor z \lor v) \), we introduce two clauses-tasks \( C_i \) and \( A_i \). We add the following precedence constraints: \( C_i \rightarrow l \) with \( l \in \{ y', \bar{z}', \bar{v} \} \) and \( l \rightarrow A_i \) with \( l \in \{ y', \bar{z}', \bar{v} \} \).
3. For all clauses of length two denoted by \( C_i = (x \lor \bar{y}) \), we introduce five clauses-tasks \( D_j \), \( j \in \{1, 2, 3, 4, 5\} \) with precedence constraints: \( D_j \rightarrow D_{j+1} \) with \( j \in \{1, 2, 3, 4\} \) and \( l \rightarrow D_5 \) with \( l \in \{ x', \bar{y}' \} \).

- Let us first assume that there is a schedule of length at most 6. In the following, we will prove that there is a truth assignment \( I : \mathcal{V} \rightarrow \{0, 1\} \) such that each clause in \( \mathcal{C} \) has exactly one true literal.

First we can remark that if a task is executed at slot 3 (resp. at slot 4) or before (resp. after) all these predecessors (resp successors) are executed on the same processor. Since the communication is allowed on a path of length 5 so that \( \forall i, \) all the clauses-tasks \( D_j \) are executed on the same processor.

**Lemma 2.** If \( C_{\text{max}} = 6 \) then if we assign the value true to the variable \( x \) if and only if the variable-task \( x' \) is executed at slot 3 we obtain a correct solution.

**Proof.** \( \forall l \in \mathcal{V} \), by construction, it is clear that:

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The variable-task \( l' \) is executed at slot 3 on the processor which executed \( \alpha_{x'} \) and \( C_i \) where \( x \in C_i \) or after slot 4 on the processor which executed \( D_{t_3}^i \) such that \( x \in C_i \).

The variable-task \( l' \) is executed at slot 3 on the processor which executed \( \alpha_{x'} \) and \( \beta_{x'} \) or after slot 4 on the processor which executed \( D_{t_3}^i \) such that \( \bar{x} \in C_i \).

The variable-task \( l' \) is executed at slot 2 or 3 on the processor which executed \( \beta_{x'} \) or after slot 4 on the processor which executed \( A_i^t \) such that \( x \in C_i \).

Using the same notation as previously, we consider

- \( X_1 = \{ l' \mid t_j = 3 \} \) where \( t_j \) designs the starting of the task \( j \).
- \( X_2 = \{ l' \mid t_j \geq 4 \} \).
- \( X_3 = \{ l' \mid t_j = 3 \} \).
- \( X_4 = \{ l' \mid t_j = 4 \} \).
- \( X_5 = \{ l' \mid t_j = 2 \) or \( t_j = 3 \} \).
- \( X_6 = \{ l' \mid t_j \geq 4 \} \).

Let be \( x_i = |X_i| \) for \( i \in \{1, \ldots, 6\} \).

We can stem from the construction of an instance of the scheduling problem,

- \( x_1 \leq \frac{n}{2} \) (there are only \( \frac{n}{2} \) clauses-tasks \( C_i \)),
- \( x_2 + x_4 \leq n \) (there are only \( n \) processors which executed a clause-task \( D_{t_3}^i \)),
- \( x_3 + x_5 \leq n \) (\( \forall x', \bar{x}' \) and \( \bar{x}' \) can not be executed together at slot 3 in the same processor (they have a common predecessor)),
- \( x_6 \leq \frac{2n}{3} \) (there are at most \( \frac{n}{3} \) processors which executed a clause-task \( A_i^t \) and each processor have only the slot 4 and 5 to execute variables-tasks \( \bar{x}' \)).

So we can deduce from \( \sum_{i=1}^{6} x_i \leq 3n \). As \( \sum_{i=1}^{6} x_i = 3n \) (all the variables-tasks must be executed), the inequalities are all equalities.

And finally, we have \( x_1 = x_4 = x_5 = \frac{n}{3} \) and \( x_2 = x_3 = x_6 = \frac{2n}{3} \).

If the variable-task \( x' \) (resp. the variable-task \( \bar{x}' \)) is executed at slot 3, the variable-task \( x' \) (resp. \( x' \)) must be executed at or after slot 4. Therefore, \( X_1 \subseteq X_4 \) and \( X_3 \subseteq X_2 \) and finally as the cardinalities are equal \( X_1 = X_4 \) and \( X_3 = X_2 \).

So if we affect the value “true” to the variable \( l \) if and only if the variable-task \( l' \) is executed at slot 3, it is trivial to see that in the clause of length 3 we have one and only one literal equal to “true”.

Let be \( c = (x \vee \bar{y}) \), a clause of length 2. As in the proof of the theorem 1, we obtain:

- If \( x' \in X_1 \implies y' \in X_4 \implies y' \in X_1 \). Only the literal \( x \) is “true” between \( x \) and \( y \),
- If \( x' \in X_2 \implies y' \in X_3 \implies y' \in X_2 \). Only the literal \( y \) is “true” between \( x \) and \( y \).

In conclusion, there is only one true literal per clause.

Conversely, we suppose that there is a truth assignment \( I : V \rightarrow \{0, 1\} \), such that each clause in \( C \) has exactly one true literal.

Suppose that the true literal in the clause \( C_i = (y \vee z \vee t) \) is \( t \). Therefore, the variable-task \( l' \) is processed at the slot 3 on the same processor which executed the clause-task \( C_i \) and the task \( \alpha_{x'} \). The task \( \bar{l}' \) (resp. \( y' \), \( z' \)) are executed on the processor which executed the clause-task \( D_{t_3}^i \) corresponding to the clauses of length 2 where \( t \) appears (resp. \( y, z \)). The clause-task \( D_{t_j}^i \), \( j = 1, 2, 3, 4 \), is executed at slot \( j \) on the same processor. The \( \frac{2n}{3} \) other variables-tasks \( y' \) not yet scheduled are executed at slot 3 in the processor which executed \( \alpha_{x'} \) and \( \beta_{x'} \). The variables-tasks \( l' \) (resp. \( y' \) and \( z' \)) are executed at slot 2 (resp. 4 and 5) on the processor executing \( \beta_{x'} \). We can observe that this scheduling is valid.

This concludes the proof of theorem 2.

We can deduce from the two previous theorems, the classical following Corollary:

**Corollary 1.** There is no polynomial-time algorithm for the problem \( \hat{P} | \text{prec}| c_{ij} = c \geq 2; p_i = 1 | C_{\text{max}} \) with performance bound smaller than \( 1 + \frac{1}{c-2} \) unless \( P \neq \mathcal{NP} \).

**Proof.** The proof of Corollary 1 is an immediate consequence of the Impossibility Theorem, (see [3], [4]).
3.3 The minimization of the sum of the completion time

In this section, we will show that there is no polynomial-time algorithm for the problem $\overline{P}_{\text{prec}; c_{ij} = c \geq 2; p_i = 1} \sum_j C_j$ with performance bound smaller than $1 + \frac{1}{2c+5}$ unless $\mathcal{P} \neq \mathcal{NP}$. This result is obtained by the polynomial transformation used for the proof of Theorem 1 and the gap technic (see [7]).

**Theorem 3.** There is no polynomial-time algorithm for the problem $\overline{P}_{\text{prec}; c_{ij} = c \geq 2; p_i = 1} \sum_j C_j$ with performance bound smaller than $1 + \frac{1}{2c+5}$ unless $\mathcal{P} \neq \mathcal{NP}$.

**Proof.** We suppose that there is a polynomial time approximation algorithm denoted by $A$ with performance guarantee bound smaller than $1 + \frac{1}{2c+5}$,

Let be $I$ the instance of the problem $\overline{P}_{\text{prec}; c_{ij} = c \geq 2; p_i = 1} \sum_j C_j$ obtained by a reduction (see Theorem 1). Let be $I'$ the instance of the problem $\overline{P}_{\text{prec}; c_{ij} = c \geq 2; p_i = 1} \sum_j C_j$ by adding $x$ new tasks from an initial instance $I$. In the precedence constraints, each $x$ (with $x > \frac{(2c+6)c+(c+4)p}{2c+6c+(c+5)p}$) new tasks is a successor of the old tasks (old tasks are from the polynomial transformation used for the proof of Theorem 1). We obtain a complete directed graph from old tasks to new tasks.

If there exists such an algorithm $A$, then it can be used to decide the existence of an independent set of size $n$.

Let $A(I')$ (resp. $A^+(I')$) the result given by $A$ (resp. an optimal result) on an instance $I'$.

1. If $A(I') < (2c+5)x + (c+4)n$ then $A^+(I') < (2c+5)x + (c+4)n$. So we can decide that there exists a scheduling of an instance $I$ with $C_{\text{max}} \leq c+4$. Indeed, if there no exists such schedule. So, for every schedule a task of an instance $I$ is executed after the time $c+5$. But, perhaps $c$ tasks are executed after $2c+6$ and so $A^+(I') > (2c+6)(c-x)$. It is impossible, indeed if $(2c+6)(c-x) \geq (c+5)x + (c+4)n$ then $x \leq \frac{(2c+6)c+(c+4)n}{2c+6c+(c+5)p}$. It is a contradiction with the hypothesis.

A contradiction with $x > \frac{(2c+6)c+(c+4)n}{2c+6c+(c+5)p}$

Thus, it exists a schedule of length $c+4$ on an old task.

2. We suppose that $A(I') \geq (2c+5)x + (c+4)n$. So, $A^+(I') \geq (2c+5)x + (c+4)n$ because an algorithm $A$ is a polynomial time approximation algorithm with performance guarantee bound smaller than $\rho < \frac{2c+6}{2c+5}$. There is no algorithm to decide whether the tasks from an instance $I$ admit a schedule of length of at the most $c+4$.

Indeed, if there exists such an algorithm, by executing the tasks at time $t = 4+2c$, we obtain a schedule with a completion time strictly less than $(5+2c)x + (4+c)n$ (there is at least one task which is executed before the time $t = c+4$). It is a contradiction since $A^+(I') \geq (2c+5)x + (c+4)n$.

## 4 A polynomial time for $C_{\text{max}} = c + 2$ with $c \in \{2, 3\}$

**Theorem 4.** The problem of deciding whether an instance of $\overline{P}_{\text{prec}; c_{ij} = c; p_i = 1} |C_{\text{max}}$ with $c \in \{2, 3\}$ has a schedule of length at most $(c+2)$ is solvable in polynomial time.

**Proof.** For $c = 2$. It easy to see that the source (resp. the sink) is executed at the slot 0 (resp. the slot $c+2$).

If a source $i$ (resp. a sink $i$) is scheduled before or at the slot 2 (resp. before or at the slot $c+1$) then the task $i$ admits only one successor (resp. only one predecessor). The other tasks must be executed as soon as possible.

5 Approximation by expansion

### 5.1 Introduction and notation

In this section, we develop a new method based on a notion of expansion of a schedule. Let us first give some notation before an explanation of the method.
Notation: We denote by $\sigma^\infty$, the UET-UCT schedule, and by $\sigma^\infty_e$ the UET-LCT schedule. Moreover, we denote by $t_i$ (resp. $t_i^e$) the starting time of the task $i$ in the schedule $\sigma^\infty$ (resp. in the schedule $\sigma^\infty_e$).

**Principle:**

An idea consisting in keeping an assignment for the tasks given by a “good” feasible schedule on an unbounded number of processors $\sigma^\infty$, and proceed to an expansion of the makespan, in order to preserve temporal distance between two tasks, $i$ and $j$ with $(i, j) \in E$, processing on two different processors respect the communications delays i.e. $t_j^e \geq t_i^e + 1 + c$.

### 5.2 Description of the method

Let be a precedence graph $G=(V,E)$, we determinate a feasible schedule $\sigma^\infty$, for the model UET-UCT, using an $(4/3)$–approximation algorithm proposed by Munier and König [12]. This algorithm gives a couple $\forall i \in V, (t_i, \pi)$ on the schedule $\sigma^\infty$ corresponding to:

- $t_i$ the starting time of the task $i$ for the schedule $\sigma^\infty$ and
- $\pi$ the processor on which the task $i$ is processed at $t_i$.

Now, we determinate a couple $\forall i \in V, (t_i^e, \pi')$ on the schedule $\sigma^\infty_e$ in the following ways:

- The starting time $t_i^e = d \times t - i = \frac{(c+1)}{2} t_i$ and,

- $\pi = \pi'$.

The justification of the expansion coefficient is given below. An illustration of the expansion is given by Figure 2.

### 5.3 Analysis of the method

**Lemma 3.** The coefficient of an expansion is $d = \frac{(c+1)}{2}$.

**Proof.** Let be two tasks $i$ and $j$ such that $(i, j) \in E$, which are processed on two different processors in the feasible schedule $\sigma^\infty$. We are interested in having a coefficient $d$ such that $t_i^e = d \times t_i$ and $t_j^e = d \times t_j$. After an expansion, in order to respect the precedence constraints and the communication delays we must have $t_j^e \geq t_i^e + 1 + c$, and so

\[
d \times t_i - d \times t_j \geq c + 1 \]

\[
d \geq \frac{c+1}{t_i - t_j} \]

\[
d \geq \frac{c+1}{2} \]
It is sufficient to choose \( d = \frac{(c+1)}{2} \).

**Lemma 4.** An expansion algorithm gives a feasible schedule for the problem denoted by \( \bar{P}|\text{prec};c_{ij} = c \geq 2; p_i = 1|C_{\text{max}} \).

**Proof.** It sufficient to check that the solution given by an expansion algorithm produces a feasible schedule for the model UET-LCT. Let be two tasks \( i \) and \( j \) such that \((i, j) \in E\). We denote by \( \pi_i \) (resp. \( \pi_j \)) the processor on which the task \( i \) (resp. the task \( j \)) is executed in the schedule \( \sigma^\infty \). Moreover, we denote by \( \pi'_i \) (resp. \( \pi'_j \)) the processor on which the task \( i \) (resp. the task \( j \)) is executed in the schedule \( \sigma'_c^\infty \). Thus
- If \( \pi_i = \pi_j \) then \( \pi'_i = \pi'_j \). Since the solution given by Munier and König [12] gives a feasible schedule on the model UET-UCT, then we have
  \[
  t_i + 1 \leq t_j \\
  \frac{2}{c+1} t_i^c + 1 \leq \frac{2}{c+1} t_j^c \\
  t_i^c + 1 \leq t_i^c + \frac{c+1}{2} \leq t_j^c
  \]
- If \( \pi_i \neq \pi_j \) then \( \pi'_i \neq \pi'_j \). We have
  \[
  t_i + 1 \leq t_j \\
  \frac{2}{c+1} t_i^c + 2 \leq \frac{2}{c+1} t_j^c \\
  t_i^c + (c+1) \leq t_j^c
  \]

**Theorem 5.** An expansion algorithm gives a \( \frac{2(c+1)}{3} \)-approximation algorithm for the problem \( \bar{P}|\text{prec};c_{ij} = c \geq 2; p_i = 1|C_{\text{max}} \).

**Proof.** We denote by \( C_{\text{max}}^h \) (resp. \( C_{\text{max}}^{opt} \)) the makespan of the schedule computed by the Munier and König (resp. the optimal value of a schedule \( \sigma^\infty \)).

In the same way we denote by \( C_{\text{max}}^h \) (resp. \( C_{\text{max}}^{opt,c} \)) the makespan of the schedule computed by our algorithm (resp. the optimal value of a schedule \( \sigma'_c^\infty \)).

We know that \( C_{\text{max}}^h \leq \frac{4}{3} C_{\text{max}}^{opt} \). Thus, we obtain

\[
\frac{C_{\text{max}}^h}{C_{\text{max}}^{opt,c}} = \frac{(c+1)}{2} \frac{C_{\text{max}}^h}{C_{\text{max}}^{opt,c}} \leq \frac{(c+1)}{2} \frac{C_{\text{max}}^h}{C_{\text{max}}^{opt}} \leq \frac{(c+1)}{2} \frac{C_{\text{max}}^{opt}}{C_{\text{max}}^{opt}} \leq \frac{2(c+1)}{3}
\]

**Remark:** The method of an expansion can be used for another problem.

6 Analysis of our results

With the result proposed in this article, we complete the table 2. We proved that in the first case, as we know, the lower bound of approximation is the same for the problems with a bounded and an unbounded number of processors in presence of large communications delays.

The new complexity results are given by Table 3.

\[1 \text{ SAT see[4]} \]
\[2 \text{ Clique see[4]} \]
\[3 \text{ J1 see Theorem 1} \]
\[4 \text{ BCG see [4, 16]} \]
### Table 3. Complexity results

<table>
<thead>
<tr>
<th>Type of machines</th>
<th>$c_{ij}$</th>
<th>$C_{\text{max}}$</th>
<th>Complexity</th>
<th>Lower bound</th>
<th>References</th>
</tr>
</thead>
<tbody>
<tr>
<td>$\bar{P}$</td>
<td>$c = 1$</td>
<td>5</td>
<td>Polynomial</td>
<td></td>
<td>[17]</td>
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<tr>
<td>$\bar{P}$</td>
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<td>6</td>
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<td>$7/6 \leq \rho$</td>
<td>[17]</td>
</tr>
<tr>
<td>$\bar{P}$</td>
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<td>3</td>
<td>Polynomial</td>
<td></td>
<td>[13]</td>
</tr>
<tr>
<td>$\bar{P}$</td>
<td>$c = 1$</td>
<td>4</td>
<td>$\mathcal{NP}$-complete$^2$</td>
<td>$5/4 \leq \rho$</td>
<td>[17]</td>
</tr>
<tr>
<td>$\bar{P}$</td>
<td>$c \in {2, 3}$</td>
<td>$c + 2$</td>
<td>Polynomial</td>
<td>Theorem 4</td>
<td></td>
</tr>
<tr>
<td>$\bar{P}$</td>
<td>$c \geq 2$</td>
<td>$c + 4$</td>
<td>$\mathcal{NP}$-complete$^3$</td>
<td>$1 + 1/(c + 4) \leq \rho$</td>
<td>Theorem 1</td>
</tr>
<tr>
<td>$\bar{P}$</td>
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<td>$c + 1$</td>
<td>Polynomial</td>
<td></td>
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<td>$\bar{P}$</td>
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<td>$\mathcal{NP}$-complete$^4$</td>
<td>$1 + 1/(c + 3) \leq \rho$</td>
<td>[4]</td>
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### Table 4. New approximation results

<table>
<thead>
<tr>
<th>Type of machines</th>
<th>$c_{ij}$</th>
<th>Approximation results</th>
<th>References</th>
</tr>
</thead>
<tbody>
<tr>
<td>$\bar{P}$</td>
<td>$c = 1$</td>
<td>$\rho \leq 4/3$</td>
<td>[11]</td>
</tr>
<tr>
<td>$\bar{P}$</td>
<td>$c = 1$</td>
<td>$\rho \leq 7/3$</td>
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<td>$\bar{P}$</td>
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<td>2 for tree</td>
<td>[9]</td>
</tr>
<tr>
<td>$\bar{P}$</td>
<td>$c \geq 1$</td>
<td>$2(c+1)$</td>
<td>Theorem 5</td>
</tr>
<tr>
<td>$\bar{P}$</td>
<td>$c \geq 2$</td>
<td>$O(c)$</td>
<td>[14]</td>
</tr>
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### 7 Conclusion

In this paper, we first proved that the problem of deciding whether an instance of $\bar{P}[\text{prec}; c_{ij} = c \geq 2; p_i = 1]C_{\text{max}}$ has a schedule of length at most $(c + 4)$ is $\mathcal{NP}$-complete.

This result is to be compared with the result of [8] and [1], which states that $\bar{P}[\text{prec}; c_{ij} = 1; p_i = 1]C_{\text{max}} = 6$ (resp. $P[\text{prec}; c_{ij} = c \geq 3; p_i = 1]C_{\text{max}} = c + 3$) is $\mathcal{NP}$-complete. Our result implies that there is no $\rho$-approximation algorithm with $\rho < 1 + \frac{1}{c+4}$, unless $\mathcal{P} = \mathcal{NP}$. In addition, we show that there is no hope of finding a $\rho$-approximation algorithm with $\rho$ strictly less than $\rho < 1 + \frac{1}{2c+5}$ for the problem of the minimization of the sum of the completion time.

Secondly, we established that the problem of deciding whether an instance of $\bar{P}[\text{prec}; c_{ij} = c; p_i = 1]C_{\text{max}}$ with $c \in \{2, 3\}$ has a schedule of length at most $(c + 2)$ is solvable in polynomial time.

We also propose a $2(c+1)$-approximation algorithm based on the notion of dilatation. **Remark:** We conjecture that the problem of deciding whether an instance of $\bar{P}[\text{prec}; c_{ij} = c; p_i = 1]C_{\text{max}}$ with $c \geq 2$ has a schedule of length at most $(c + 3)$ is solvable in polynomial time.

### References