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Residue systems efficiency for modular products summation: Application to Elliptic Curves Cryptography

JC Bajard\textsuperscript{a}, S. Duquesne\textsuperscript{b}, M Ercegovac\textsuperscript{c} and N Meloni\textsuperscript{a}\textsuperscript{b}

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ABSTRACT
Residue systems of representation, like Residue Number Systems (RNS) for primary field ($GF(p)$) or Trinomial Residue Arithmetic for binary field ($GF(2^k)$), are characterized by efficient multiplication and costly modular reduction. On the other hand, conventional representations allow in some cases very efficient reductions but require costly multiplications.

The main purpose of this paper is to analyze the complexity of those two different approaches in the summations of products. As a matter of fact, the complexities of the reduction in residue systems and of the multiplication in classical representations are similar. One of the main features of this reduction is that it doesn’t depend on the field. Moreover, the cost of multiplication in residue systems is equivalent to the cost of reduction in classical representations for special well-chosen fields.

Taking those properties into account, we remark that an expression like $A \ast B + C \ast D$, which requires two products, one addition and one reduction, evaluates faster in a residue system than in a classical one. So we propose to study types of expressions to offer a guide for choosing a most appropriate representation.

One of the best domain of application is the Elliptic Curves Cryptography where addition and doubling points formulas are composed of products summation. The different kinds of coordinates like affine, projective, and Jacobean, offer a good choice of expressions for our study.

Keywords: Elliptic Curve Cryptography (ECC), modular addition, modular multiplication, modular reduction, Residue Number System (RNS), hardware implementation

1. MULTIPLICATION AND REDUCTION PROBLEMS
In the expressions using modular arithmetic in domains like cryptography we find combinations of multiplications and additions. The modular reductions are not necessarily linked to the operations, i.e., we can perform several operations before modular reduction. This is particularly true for a series of additions, where the growth of the numbers involved is slow.

In multiplications, however, the growth of precision is fast since the number of digits in the product is the sum of those of the operands. Most of the time multiplications are followed by additions, allowing minimizing the number of reductions. This is true for any number representations, but it is clear that the faster the operations are, the better the expected gain would be, even if the reduction is costly.

1.1. Cost of Multiplication
Since we are considering Elliptic Curve Cryptography (ECC), we assume that the operands don’t need more than 512 bits. Hence, we analyze only one multiplication algorithm: the Basic (paper-and-pencil) Method. This is based on the GMP recommendations\textsuperscript{12}. Toom-Cook method\textsuperscript{14} is only applicable for huge numbers not used in the ECC, and Karatsuba method\textsuperscript{14} is more appropriate for software implementation (like in GMP) than for dedicated architectures we are considering.
Table 1. Thresholds of method usefulness given in number of words (bits)

<table>
<thead>
<tr>
<th>Architecture</th>
<th>word size</th>
<th>Karatsuba</th>
<th>Toom-Cook</th>
</tr>
</thead>
<tbody>
<tr>
<td>AMD K7</td>
<td>32</td>
<td>26 (832)</td>
<td>202 (6464)</td>
</tr>
<tr>
<td>Pentium 4</td>
<td>32</td>
<td>18 (576)</td>
<td>139 (4448)</td>
</tr>
<tr>
<td>PowerPC 32</td>
<td>32</td>
<td>20 (640)</td>
<td>226 (7232)</td>
</tr>
<tr>
<td>SPARC v7 32</td>
<td>32</td>
<td>8 (256)</td>
<td>466 (14912)</td>
</tr>
<tr>
<td>PowerPC 64</td>
<td>64</td>
<td>8 (512)</td>
<td>57 (3648)</td>
</tr>
<tr>
<td>ultrasp arc II</td>
<td>64</td>
<td>22 (1408)</td>
<td>98 (6272)</td>
</tr>
<tr>
<td>IA64</td>
<td>64</td>
<td>47 (3008)</td>
<td>288 (18432)</td>
</tr>
</tbody>
</table>

To summarize, depending on the architecture of the processors, the Basic Method is preferable up to an average of 16 words (512 bits, for a 32 bits architecture), see Table 1.

In this paper, we consider a $k$-bit word architecture, with $k = 16, 32$ or 64. Let $\beta = 2^k$. Then, a large integer is represented with radix $\beta$ as $A = \sum_{i=0}^{n-1} a_i \beta^i$, having a length of $kn$ bits.

1.1.1. The Basic Method

The Basic Method is best performed with the Horner scheme:

**Algorithm 1**: Basic($A, B$)

**Data**: $A = \sum_{i=0}^{n-1} a_i \beta^i$ and $B = \sum_{i=0}^{n-1} b_i \beta^i$;

**Result**: $P$ such that $P = A \times B$;

1. $P \leftarrow 0$;
2. for $i = n - 1$ to 0 do
   1. $(P, P) \leftarrow a_i \times B$;
   2. $P \leftarrow (P + P) \beta + P$
3. return $P$

Line 1, the digit-by-digit product $a_i \times b_j$ consists two $k$-bit words, the upper and the lower one. The product $a_i \times B$ result in upper and lower parts, stored in two $n$-words numbers $P$ and $P$ which are added to the partial result $P$. In this form we have a better idea of the number of additions needed.

The cost of the Basic Method is $n^2$ products of two $k$-bits words giving a result on two words, and $2n^2 - n$ additions (with a carry) of two $k$-bits words. We don’t take into account the carry propagation.

1.1.2. About parallel implementation

In fact, in the literature, only parallel implementations of the Basic Method are proposed. In this algorithm when we have at our disposal $n^2$ multipliers, all the products ($n$ times line 1 of algorithm 1) can be done in parallel. Then with a tree of adders, all the additions can be computed in a logarithmic time. With some tricks the area can be reduced in $O(n^2 / \log^2(n))^B$.

In a configuration with $n$ arithmetic units, we can perform in parallel the $n$ products of each call to line 1 of the Basic algorithm. Then, to avoid the carry propagation, we propose to accumulate the carries of the previous in a $n$-words variable $R$ initialized to 0 (a kind of carry save approach). Thus the line 2 of Algorithm 1, becomes:

$$ (P, R) \leftarrow (P + P) \beta + P $$

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At the end a carry-propagate addition produces \( P \leftarrow P + R \).

The time complexity of this kind of implementation is \( n \) word-products and \( 3n \) word-additions (taking into account the accumulation of carries) and a final carry-propagate addition of two numbers of \( 2n \) words.

1.2. Modular reduction algorithms

1.2.1. Pseudo-Mersenne moduli

The NIST recommendations propose to use moduli which satisfy the Pseudo-Mersenne property:

\[
N = \beta^n - c, \text{ with } c < \beta^{\frac{n}{2}}, \text{ and } w_H(c) < t
\]

where \( w_H \) represents the Hamming weight and \( t \) is a small number. We remark that \( c \) can be written using sign digits. In this case \( w_H \) represents the non-zero digits.

The reduction can be done with three additions and two (half) products by \( c \). If \( c \) is composed of \( w_H(c) \) non-zero digits then those operations can be done by \( w_H(c) \) additions.

If \( X < \beta^{2n} \) then

\[
X = X_1\beta^n + X_0 \equiv c \times X_1 + X_0 = X'
\]

Now \( X' < \beta^{\frac{3n}{2}} \), we reiterate the previous process

\[
X' = X'_1\beta^n + X'_0 \equiv c \times X'_1 + X'_0 = X''
\]

Thus \( X'' < 2\beta^n \), and a last reduction could be useful. In this case we just have to consider the bit \( r \) of weight \( \beta^n \):

If \( r = 1 \) then \( X \pmod{N} = (X'' + c) \mod \beta^n \)
else \( \tilde{X} = (X'' + c) \mod \beta^n \) and \( r' = (X'' + c) \div \beta^n \);
if \( r' = 1 \) then \( X \pmod{N} = \tilde{X} \) else \( X \pmod{N} = X'' \).

To summarize: the cost of this reduction is two multiplications of \( \frac{n}{2} \times n \) words integers and three \( n \)-words additions, or if \( t \) is small, lower than \( 3 + 2t \) \( n \)-words additions. Many approaches around this class of numbers have been considered such as in\(^{9,21}\).

1.2.2. Montgomery approach

This reduction is available when we use the Montgomery representation: An integer \( X \) is represented by the value \( \tilde{X} = X \times \beta^n \pmod{N} \).

Hence when a reduction occurs after a multiplication we get \( \tilde{X} \times \tilde{Y} \equiv XY \times \beta^{2n} \times \beta^{-n} \pmod{N} \equiv XY \times \beta^n \pmod{N} \), which is a Montgomery representation of the product \( XY \).

Algorithm 2: \( \text{Montgomery}_N(R) \)

\[
\text{Data: } R = \tilde{X} \times \tilde{Y} < N^2 < \beta^{2n} \text{ and } \beta^{n-1} \leq N < \beta^n \\
\text{and a precomputed value } (-N^{-1} \mod \beta^n); \\
\text{Result: } R' = R\beta^{-n} \pmod{N} < 2N; \\
Q \leftarrow R \times (-N^{-1}) \mod \beta^n; \\
R' \leftarrow (R + QN) / \beta^n;
\]

We can also consider a digit version of this algorithm:
Algorithm 3: Montgomery\(D_N(R)\)

**Data:** \(R = \tilde{X} \times \tilde{Y} < N^2 < \beta^{2n}\) and \(\beta^{n-1} \leq N < \beta^n\) and a precomputed value \((-n_0^{-1}\mod\beta)\);  

**Result:** \(R' = R\beta^{-n} \mod N < 2N\);  
\(R' = R'\)

for \(i = 0\) to \(n-1\) do

1. \(q = r_0 \times n_0^{-1} \mod \beta\);  
2. \(R' = (R' + qN) / \beta\);

end

In line 1, we perform a multiplication of two words where only the lower word of the result is considered. Then in line 2, we multiply a \(n\) words value by a word, as described before, the result can be stored in two \(n\) words values, one for the lower parts and one for the upper one. That means that we have to perform \(n\) words products. Then, those results have to be added to \(R\), that represents \(2(n-1)\) words additions. Here we don’t take into account the carry propagation. Hence, the total cost is \(n^2 + n\) words products and \(2n(n-1)\) words additions.

Now, for a parallel implementation on \(n\) arithmetic units we can store the carries in a variable \(C\), we only have to evaluate exactly \(r_0\) using \(c_0\) (\(C\) is shifted like \(R\)). Thus, line 2 is reduced to one step of \(n\) words products and two steps of \((n-1)\) words additions. At the end, \(C\) must be added to \(R\).

Montgomery algorithm is actually the most often implemented method available for any modulo \(7, 15, 19\). It is particularly interesting for general architecture not dedicated to only one modulo, unlike the pseudo-Mersenne ones. This is the reason why it is so popular.

2. RNS PROPERTIES

2.1. A short introduction

The Residue Number System (RNS)\(^{11,24}\) is discussed in detail in\(^{22}\) and\(^{14}\). The RNS is based on the Chinese remainder theorem which allows to represent an integer \(X < M\) by the set \((x_1, \ldots, x_n)\) where \(M = \prod_{i=1}^n m_i\) with \((m_i, m_j) = 1\) for \(i \neq j\), and the residues \(x_i = X \mod m_i\).

The use of RNS is well-known in signal processing\(^{13,23}\) and cryptography\(^{4,10}\). The main advantage of those systems is due to the fact that additions and multiplications are done independently on the residues. In other words, with a parallel architecture with \(n\) arithmetic units, the time needed to perform an addition or a multiplication is bounded by one modular operation on the largest residue. Therefore the choice of the set \((m_1, \ldots, m_n)\), called RNS basis, is very important, as we have to reduce modulo \(m_i\). In\(^6\), the authors propose some criteria for selecting a ”good” RNS bases.

2.2. Choice of Moduli

To have better performance, the moduli must be pseudo-Mersenne numbers, namely of the form \(2^k - c\) with \(c\) less than \(2^{k/2}\) and it is preferable that the maximal weight of \(c\) in signed digit representation (denoted by \(a\)) is as small as possible. In this section, we are interested in finding sufficiently many such integers which are pairwise coprime.

To take into account the improvement of the change of basis presented in\(^6\), one wants to have consecutive moduli. We will denote by \(\delta\) the amplitude of the set of moduli constructed. In general, such a set is split in order to construct two basis. Then, the interesting value is \(\delta_{\text{max}}\) which is the maximum of the magnitudes of the two basis.

We will deal with two sizes of numbers. On one hand we want numbers less than \(2^{16}\) to use RNS arithmetic on 16-bit architectures and, on the other hand, we want numbers less than \(2^{32}\) to use RNS arithmetic on 32-bit architectures.
2.2.1. Case of 16 bits words

It is of course not easy to find a set of such pairwise coprime numbers. If $a = 3$, it is not possible to find a set with more than 21 elements. We give here only the value of $c$.

\{8, 15, 17, 27, 29, 39, 47, 57, 59, 63, 95, 113, 119, 123, 125, 127, 129, 131, 135, 137, 143\}.

If $a = 4$ one can find a set of 40 such numbers (and no more).


Without any condition on $a$, the largest possible set has 48 elements. Since in a $n$-words RNS arithmetic it is necessary to have a set with $2^n$ elements, it is not possible to perform RNS arithmetic with more than 24 16-bits words.


Finally, the difference between two numbers of the same set is always less than $2^8$ by construction so that the improvements on the RNS change of basis given in⁶ can be done. Moreover, it is possible to split the sets above in two parts such that the difference between two numbers in each subset is even smaller. For instance, if one wants to perform ECC 160, one can choose \{8, 15, 17, 27, 29, 39, 47, 57, 59, 63\} and \{113, 119, 123, 125, 127, 129, 131, 135, 137, 143\} so that $\delta_{\text{max}} < 2^6$.

2.2.2. Case of 32 bits words

There are more choices in this case. If $a = 3$ the maximal size of a suitable set is 87 and if $a = 4$ one can find 448 numbers which are pairwise coprime. This is more than necessary to deal with very large numbers. We do not give the sets here since they are easy to find, using for instance the program available at http://www.math.univ-montp2.fr/~duquesne.

However the improvement given in⁶ cannot be used since we only have by construction that $\delta \leq 2^{16}$. We will now take into account the constraint that the difference between two numbers of the set must be less than $\delta$.

A good value for $\delta$ is $2^8$. If $a = 4$, one can find 47 such numbers


This is more than required by elliptic curve cryptography since 32 numbers are sufficient. It is easy to extract from this set two bases of 16 moduli such that $\delta_{\text{max}} < 2^7$, two bases of 8 moduli such that $\delta_{\text{max}} < 2^5$ or two basis of 6 moduli such that $\delta_{\text{max}} < 2^4$. Hence elliptic curve cryptography can be very efficiently implemented with this kind of moduli.

In order to obtain better performance, one can choose $a = 3$. But, in this case, one can find only 25 numbers which are pairwise coprime and satisfy the constraint on $\delta$. It is not enough. If one wants to perform RNS arithmetic with large numbers one must relax the constraint. If we take $\delta = 2^9$ one can find 32 numbers with $a = 3$


Of course, it is not possible to have $\delta_{\text{max}} < \delta$ for two bases of 16 moduli, but one can again obtain $\delta_{\text{max}} < 2^5$ for two bases of 8 moduli and $\delta_{\text{max}} \leq 2^4$ for two bases of 6 moduli. We summarize in the table 2 the possibilities for performing RNS arithmetic.
2.3. The RNS Montgomery Reduction

An efficient RNS modular reduction was proposed in $^2$. This approach is a translation in RNS of the algorithm 2, where operations are performed in RNS.

Algorithm 4: RNS Montomery$_N(R, \tilde{R})$

Data: Two RNS bases $B_m = \{m_1, \ldots, m_n\}$, where $M = \prod_{i=1}^{n} m_i$; and $\tilde{B}_m = \{\tilde{m}_1, \ldots, \tilde{m}_\tilde{n}\}$, where $\tilde{M} = \prod_{j=1}^{\tilde{n}} \tilde{m}_j$ where $\gcd(M, \tilde{M}) = 1$ and $M < \tilde{M}$; $N$ known in the two bases, with $\gcd(N, M) = 1$, and $0 < (n + 2) N < M$.

Result: $R \equiv R \times N^{-1} \pmod{N}$ expressed in the two RNS bases with $R \equiv R \times M^{-1} \pmod{N}$

1. $Q \leftarrow R \times N^{-1}$ in $B_m$;
2. Extension 1: $Q \rightarrow \tilde{Q}$ from $B_m$ to $\tilde{B}_m$;
3. $\tilde{R} \leftarrow (\tilde{R} \times N \times (\tilde{Q} \times N)) \times N^{-1} \pmod{\tilde{M}}$ in $\tilde{B}_m$;
4. Extension 2: $\tilde{R} \leftarrow R$ from $\tilde{B}_m$ to $B_m$.

The drawback of this algorithm is due to the inversion of $M$, which is not possible in its basis, so a bases extension is needed. We analyze those extensions in the next section.

2.4. Bases extensions

The first extension takes the Lagrange formulation:

$$\tilde{Q} = \sum_{i=1}^{n} q_i \frac{|M_i|^{-1}}{m_i} M_i = Q + \alpha M$$

(4)

for some value of $\alpha$ where $0 \leq \alpha < n$. Thus, this computing is done for each modulo $\tilde{m}_j$ of the basis $\tilde{B}_m$. Hence, the first extension does not really give the representation of $Q$ in $\tilde{B}_m$, but the one of $\tilde{Q}$. That gives the condition $0 < (n + 2) N < M$.

Now for the second extension, we use the same approach with $\tilde{R}$, which is such that: $\tilde{R} \equiv R \pmod{N}$.

$$\tilde{R} = \sum_{i=1}^{n} \tilde{r}_i \frac{|\tilde{M}_i|^{-1}}{\tilde{m}_i} \tilde{M}_i = \tilde{R} + \alpha \tilde{M}$$

(5)

But, using an extra modulus $m_x > n^{20}$ (most of the time, $m_x$ is a small power of 2), we can evaluate $\alpha^{20}$:

$$\alpha = \left[ |\tilde{M}|^{-1}_{m_x} \left( \sum_{i=1}^{n} \tilde{r}_i \left| \tilde{M}_i \right|^{-1}_{\tilde{m}_i} \tilde{M}_i - \tilde{R} \right) \right]_{m_x}$$

(6)

<table>
<thead>
<tr>
<th>16 bits word</th>
<th>words</th>
<th>bits</th>
<th>32 bits words</th>
<th>words</th>
<th>bits</th>
</tr>
</thead>
<tbody>
<tr>
<td>$a = 3$</td>
<td>10</td>
<td>160</td>
<td>$a = 3$</td>
<td>43</td>
<td>1376</td>
</tr>
<tr>
<td>$a = 3, \delta_{\text{max}} &lt; 2^9$</td>
<td>16</td>
<td>512</td>
<td>$a = 3, \delta_{\text{max}} &lt; 2^8$</td>
<td>12</td>
<td>384</td>
</tr>
<tr>
<td>$a = 3, \delta_{\text{max}} &lt; 2^9$</td>
<td>8</td>
<td>256</td>
<td>$a = 3, \delta_{\text{max}} &lt; 2^8$</td>
<td>6</td>
<td>192</td>
</tr>
<tr>
<td>$a = 4$</td>
<td>12</td>
<td>192</td>
<td>$a = 4$</td>
<td>224</td>
<td>7168</td>
</tr>
<tr>
<td>$a = 4, \delta_{\text{max}} &lt; 2^9$</td>
<td>23</td>
<td>736</td>
<td>$a = 4, \delta_{\text{max}} &lt; 2^8$</td>
<td>16</td>
<td>512</td>
</tr>
<tr>
<td>$a = 4, \delta_{\text{max}} &lt; 2^9$</td>
<td>8</td>
<td>256</td>
<td>$a = 4, \delta_{\text{max}} &lt; 2^8$</td>
<td>6</td>
<td>192</td>
</tr>
</tbody>
</table>

Table 2. number of words for which RNS is possible
Thus it is now possible to compute \( r_j = |R|m_j \) by

\[
  r_j = \sum_{i=1}^{n} \left| \tilde{r}_i \left[ M_i^{-1} \right]_{\tilde{m}_i} \tilde{M}_i \right| - \left| \alpha\tilde{M} \right|_{m_j}
\]  

(7)

2.5. Complexity

2.5.1. First extension

This extension is made using formula (4) for each \( \tilde{m}_j \) and for \( m_z \). So, that needs \( n \) products mod \( m_i \) for the \( q_i \left| M_i^{-1} \right|_{\tilde{m}_i} \), and \( n^2 + n \) products by the \( M_i \) modulo \( m_j \), and \( n^2 - 1 \) additions.

If we consider an architecture with \( n \) \(+1\) but for small value \( m_x \), that is done in a time equivalent to \( n + 1 \) modular products and \( n \) additions.

Now if we consider that a modular product costs 1 product and \( an \) additions, the complexity is \( n^2 + 2n \) words products and \( n^2(a + 1) + na - 1 \) words additions. In parallel, the time complexity is equivalent to \( n + 1 \) words products and \( n(a + 1) + a \) words additions.

2.5.2. Second extension

Now we use the formulas (4) and (6) which give the same complexity as for the first extension, plus one addition and one modular product for the evaluation of \( \alpha \) and \( n \) modular products and \( n \) additions for the last reduction in equation (7).

Thus we obtain \( n^2 + 3n \) words products and \( n^2(a + 2) - 1 \) words additions. In parallel implementation, the time complexity is equivalent to \( n + 1 \) words products and \( n(a + 1) + a \) words additions.

2.5.3. Total cost of the RNS reduction

The complexity of lines 1 and 2 is due to RNS operations, thus we must perform \( 3n \) words modular products and \( n \) words modular additions. Since the cost of the reduction is \( n \) words additions, we get \( 3n \) words products and \((3a + 1)n \) words additions.

Hence, the total cost is \( 2n^2 + 8n + 1 \) words modular products and \( 2n^2 + 2n - 1 \) words modular additions. Thus the complexity is \( 2n^2 + 8n + 1 \) words products and \( 2n^2 + 2n - 1 + a(2n^2 + 6n) \) words additions (\( m_x \) is a power of 2 and the reductions of the additions are included in those of the products).

3. STUDY OF DIFFERENT VARIETIES OF FORMULA

Here we propose to compare our approach in terms of multiplications which are more costly than additions. Furthermore, the \( a \) additions of the modular product reduction can be easily included in the product generation in dedicated architecture (i.e., on FPGAs\(^1\)). We summarize in Tables 3 and 4 the different complexity found for the global cost of the methods and the time complexity for a parallel implementation with \( n \) arithmetic units.
### Table 3. Number of words operations

<table>
<thead>
<tr>
<th>Method</th>
<th>words-products</th>
<th>words-additions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Basic</td>
<td>$n^2$</td>
<td>$2n^2 - n$</td>
</tr>
<tr>
<td>Pseudo Mersenne</td>
<td>2B</td>
<td>3n</td>
</tr>
<tr>
<td>Pseudo Mersenne</td>
<td>0</td>
<td>$(3+2t)n$</td>
</tr>
<tr>
<td>Montgomery</td>
<td>$n^2 + n$</td>
<td>$2n(n-1)$</td>
</tr>
<tr>
<td>RNS multiplication</td>
<td>$2n$</td>
<td>$2an$</td>
</tr>
<tr>
<td>RNS Montgomery</td>
<td>$2n^2 + 8n + 1$</td>
<td>$2n^2 + 2n - 1 + a(2n^2 + 6n)$</td>
</tr>
</tbody>
</table>

### Table 4. Parallel implementation: Number of words operations

<table>
<thead>
<tr>
<th>Method</th>
<th>words-products</th>
<th>words-additions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Basic</td>
<td>$n$</td>
<td>$3n + (2n \text{ or } \log(n))$</td>
</tr>
<tr>
<td>Pseudo Mersenne</td>
<td>2B</td>
<td>$3n \text{ or } \log(n)$</td>
</tr>
<tr>
<td>Pseudo Mersenne</td>
<td></td>
<td>$(3 + 2t)n$</td>
</tr>
<tr>
<td>Montgomery</td>
<td>$2n$</td>
<td>$2n + 1$</td>
</tr>
<tr>
<td>RNS multiplication</td>
<td>2</td>
<td>$2a$</td>
</tr>
<tr>
<td>RNS Montgomery</td>
<td>$2n + 7$</td>
<td>$2n + 1 + a(2n + 6)$</td>
</tr>
</tbody>
</table>

### 3.1. Formulas of the type $\sum_{i=1}^{s} A_i B_i \mod N$

Whatever the representation of the values involved in the calculation of a such an expression, it is clear that the best is to compute reductions mod $N$ only when necessary. In this case, we consider that only one reduction is necessary.

Indeed, that modifies somewhat the conditions depending on the reduction method. For example, with the Montgomery method for summing $\log_2 s$ extra rounds could be necessary, or with the pseudo-Mersenne $c$ must be smaller than $\beta^{n/2 - \log_2 s}$.

Thus, the complexity of the evaluation of the kind of formulas is equivalent to $s$ products and $s-1$ additions following by only one reduction. In Table 5 we summarize the cost as a function of $s$ of the different combinations which could be used to compute our expression: Basic + Pseudo Mersenne (B+PM), Basic + Montgomery (B+Montg), and RNS product + RNS Montgomery (RNS).

<table>
<thead>
<tr>
<th>Combination</th>
<th># products</th>
<th>parallel cost over $n$ units</th>
</tr>
</thead>
<tbody>
<tr>
<td>B+PM</td>
<td>$s \times n^2$</td>
<td>$s \times n$</td>
</tr>
<tr>
<td>B+Montg</td>
<td>$(s+1) \times n^2 + n$</td>
<td>$(s+2) \times n$</td>
</tr>
<tr>
<td>RNS</td>
<td>$(2s+8) \times n + 2n^2 + 1$</td>
<td>$2s + 2n + 8$</td>
</tr>
</tbody>
</table>

### Table 5. Number of products in the evaluation of $\sum_{i=1}^{s} A_i B_i \mod N$

If we consider the global cost of these different approaches, we could evaluate for which value of $s$ RNS becomes more efficient. First we deal with B+PM, in which case we have:

\[
(2s+8) \times n + 2n^2 + 1 < s \times n^2
\]  \hspace{1cm} (8)

\[
s > \frac{2n^2 + 8n + 1}{n^2 - 2n}
\]  \hspace{1cm} (9)

Thus, for $s = 3$ the RNS is better as long as $15 \leq n$, and $s = 4$ is required if $9 \leq n \leq 14$. Now, we could
compare to B+Montg with the RNS approach:

$$\begin{align*}
(2s + 8) \times n + 2n^2 + 1 &< (s + 1) \times n^2 + n \\
\frac{n^2 + 7n + 1}{n^2 - 2n} &> s
\end{align*}$$

Here, RNS is most efficient for \( s \geq 2 \) and \( n \geq 12 \), \( s \geq 3 \) and \( 7 \leq n \leq 11 \) or \( s \geq 4 \) with \( n = 6 \).

The RNS is remarkable when we consider a parallel implementation on \( n \) arithmetic units. Compared to B+PM, RNS begin to be efficient when \( s \geq 3 \) for \( n \geq 13 \) (the condition is \( s \geq \frac{2n+7}{n-2} \)). But if we deal with general architectures available for many fields, B+Montg is preferable. In this case RNS is better when \( s > 0 \) for \( n \geq 9 \) (the condition is \( s \geq \frac{7}{n-2} \)).

### 3.2. Application to ECC

Let \( p \) be a prime number and \( E : y^2 = x^3 + ax + b \) be an elliptic curve defined over \( \mathbb{F}_p \). Let also \( P = (X_p, Y_p, Z_p) \) and \( Q = (X_q, Y_q, Z_q) \) \( E(\mathbb{F}_p) \) given in projective coordinates. Assume that the difference \( P - Q = (x, y) \) is known in affine coordinates. Then one can obtain the \( X \) and \( Z \)-coordinates for \( P + Q \) and \( 2P \) in terms of the \( X \) and \( Z \)-coordinates for \( P \) and \( Q \) by the following formulas:

- \( X_{p+q} = -4bZ_pZ_q(X_pZ_q + X_qZ_p) + (X_pX_q - aZ_pZ_q)^2 \),
- \( Z_{p+q} = x((X_pZ_q + X_qZ_p)^2 - 4X_pX_qZ_pZ_q) \),
- \( X_{2p} = (X_p^2 - aZ_p^2)^2 - 8bX_pZ_p^3 \),
- \( Z_{2p} = 4X_pZ_p(X_p^2 + aZ_p^2) + 4bZ_p^3 \).

In\(^3\) we proposed to compute \( X_{p+q} \) and \( Z_{p+q} \) using the following operations:

1. \( \alpha = Z_pZ_q \)
2. \( \beta = X_pZ_q + X_qZ_p \)
3. \( \gamma = X_pX_q \)
4. \( \delta = -4b\alpha \)
5. \( X_{p+q} = \beta\delta + (\gamma - a\alpha)^2 \)
6. \( \epsilon = \beta^2 - 4\alpha\gamma \)
7. \( Z_{p+q} = x\epsilon \)

To compute \( X_{2p} \) and \( Z_{2p} \), the following operations must be done:

1. \( \alpha = Z_p^2 \)
2. \( \beta = 2X_pZ_p \)
3. \( \gamma = X_p^2 \)
4. \( \delta = -4b\alpha \)
5. \( X_{2p} = \beta\delta + (\gamma - a\alpha)^2 \)
6. \( Z_{2p} = 2\beta(\gamma + a\alpha) - \alpha\delta \)

The scalar point multiplication over \( E \) is performed using the Montgomery ladder\(^{17,16}\) which involves an addition and a doubling at each step of the algorithm. With the previous formulae, this operation can be done in 17 multiplications and 13 modular reductions.

So, one step of the Montgomery exponentiation algorithm using the previous formulae requires \( 18(2n) + 13(2n^2 + 8n) \) operations in RNS, \( 17n^2 + 14(a^2 + n) \) with the Montgomery modular multiplication and \( 17n^2 \) with Mersenne numbers.

We can see that in practice our approach is slower than both methods for standard length (160-192). We make two remarks: first, compared to the Montgomery modular multiplication, we are asymptotically better; second, the problem comes from the fact that those formulae remain simple and involve only 4 independent coordinates, which limits the possibility of obtaining long sums of products. This suggests that our approach may be more suited to hyperelliptic curves (which can involve 12 different parameters).
<table>
<thead>
<tr>
<th>$p_{12}$ word</th>
<th>RNS</th>
<th>Montgomery</th>
<th>Mersenne</th>
</tr>
</thead>
<tbody>
<tr>
<td>160</td>
<td>5</td>
<td>1350</td>
<td>845</td>
</tr>
<tr>
<td>192</td>
<td>6</td>
<td>1776</td>
<td>1200</td>
</tr>
<tr>
<td>256</td>
<td>8</td>
<td>2784</td>
<td>2096</td>
</tr>
<tr>
<td>320</td>
<td>10</td>
<td>4000</td>
<td>3240</td>
</tr>
<tr>
<td>512</td>
<td>16</td>
<td>8896</td>
<td>8160</td>
</tr>
</tbody>
</table>

Table 6. Number of word size multiplications for one step of Montgomery exponentiation algorithm

4. CONCLUSIONS

In this study we have shown that the cost of the reduction in the RNS is compensated by fast evaluation of the product and the sum. Whenever more than two products have to be added, the RNS is a good alternative and if the number of terms of the sum increases, it becomes clearly the most efficient method.

One domain of application for this approach is the Elliptic Curve Cryptography where expressions of points addition formulae can be rewritten to take into account the RNS specificities. These expressions can be improved and they could be extended to hyper-elliptic curves approaches.

Another point that could be analyzed is the number and the size of the RNS bases elements. It could be interesting to mix the basic approach with the RNS one. For example, we could consider $m_i$ as pseudo-Mersenne numbers of 128 bits on which products and additions are made with the classical approach. In this cases the number of elements of the RNS bases should be small, for example 4, and we could perform the operations in the RNS and then convert into classical representation for the reduction. In fact, this could be used also for the modulo calculation for each $m_i$ which will be part of our future research.

REFERENCES