

Noise Optimisation of a Piezoresistive CMOS MEMS for Magnetic Field Sensing

Vincent Beroulle, Yves Bertrand, Laurent Latorre, Pascal Nouet

▶ To cite this version:

Vincent Beroulle, Yves Bertrand, Laurent Latorre, Pascal Nouet. Noise Optimisation of a Piezoresistive CMOS MEMS for Magnetic Field Sensing. SOC Design Methodologies, 90, Kluwer Academic Publishers, pp.461-472, 2002, IFIP - The International Federation for Information Processing, 978-1-4757-6530-4. $10.1007/978-0-387-35597-9_40$. lirmm-00268487

HAL Id: lirmm-00268487 https://hal-lirmm.ccsd.cnrs.fr/lirmm-00268487v1

Submitted on 1 Oct 2022

HAL is a multi-disciplinary open access archive for the deposit and dissemination of scientific research documents, whether they are published or not. The documents may come from teaching and research institutions in France or abroad, or from public or private research centers.

L'archive ouverte pluridisciplinaire **HAL**, est destinée au dépôt et à la diffusion de documents scientifiques de niveau recherche, publiés ou non, émanant des établissements d'enseignement et de recherche français ou étrangers, des laboratoires publics ou privés.

Noise optimisation of a piezoresistive CMOS MEMS for magnetic field sensing

Vincent Beroulle, Yves Bertrand, Laurent Latorre, Pascal Nouet

Laboratoire d'Informatique, de Robotique et de Microélectronique de Montpellier (France) Contact author : latorre@lirmm.fr

Abstract:

Using 100% industrial fabrication processes, the design and the fabrication of a monolithic CMOS MEMS magnetic field sensor, targeting noise reduction, are developed in this paper. The sensor is based on a resonant cantilever structure with optimized electronics for signal treatment and noise filtering in order to achieve competitive performances. With a final sensitivity of $350V_{rms}/T$ and a $2\mu T$ resolution, the system is able to measure earth natural magnetic field.

Key words: CMOS MEMS, magnetic sensor, noise

1. INTRODUCTION

Magnetic field sensing devices regain interest since new applications have been demonstrated for automotive systems including navigation and mechanical parts (e.g. wheels) motion detection (ABS). Because magnetic sensing does not involve mechanical contact and wear, it offers the reliability level required by security concerns.

In such applications, major concerns are reliability, performances and cost. Monolithic integration of intelligent sensing devices using a standard microelectronic process (CMOS) provides significant cost reduction leading to high volume production. Also reliability is improved by reducing the number of wired connections. Regarding performances, it is obvious that the best magnetic field sensor will be achieved using dedicated processes (e.g. GMR [1]), however, the purpose of this paper is to show that using a CMOS

The original version of this chapter was revised: The copyright line was incorrect. This has been corrected. The Erratum to this chapter is available at DOI: 10.1007/978-0-387-35597-9_40

MEMS sensing device, on-chip signal treatment allows for overall performance improvement.

Electromechanical CMOS magnetic field sensors have been under investigation in our laboratory for several years [2]. The first part of the paper is dedicated to the electromechanical cell: the "U-Shape" cantilever. Sensing principle, and modeling are addressed with detail on the methodology we used to integrate the structure into a standard ASIC design flow.

The second part concerns an evaluation of the sensor performance, using a stand-alone cantilever device first, and with dedicated low-noise electronics for signal treatment. Sensitivity and resolution of overall magnetic field sensing system is finally given.

2. THE U-SHAPE CANTILEVER DEVICE

The "front-side bulk micromachining" (FSBM) technology is today fully industrialized and made available to fabless designers thought multi-project services for cheap prototyping [3]. This technology is based on a simple and low-cost post-process that etches selected parts of the CMOS die silicon substrate, releasing structures such as beams or membranes. Force sensing or thermal effects related applications might then be addressed using those structures.

From our point of view, monolithic MEMS is a standard mixed-signal ASIC that includes mechanical analog cells (beams, membranes...), the same way as it includes resistances or capacitors. Integrated system design is to be performed with standard EDA CAD tools including schematic capture, system level simulation and fabrication masks layout edition. Considering electromechanical blocs, this approach supposes that basic components have been previously characterized and translated into standard cells data with associated symbols, simulation models and layout synthesis helps.

Thus, in the following, the structure we used for magnetic field sensing is studied in order to provide the design environment with these required information.

2.1 Sensing principle

The proposed magnetic field sensor is based on the interaction between the unknown external magnetic field B to be measured and a known current I generated into the device itself (current loop).

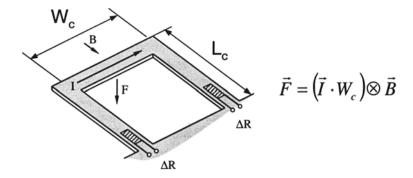


Figure 1: Magnetic field sensing principle.

The basic mechanical structure (Figure 1) consists in a U-shaped cantilever embedding an aluminum planar coil. The coil is supplied with an electrical current I that can be continuous (static mode) or alternative (dynamic mode). When an external magnetic field B is present, cantilever beam is therefore deflected due to the action of the Lorentz force F applied to its free side. Two polysilicon strain gauges are located into the mechanical structure close to anchor points in order to detect such deflections with a maximum sensitivity.

2.2 Modeling

As illustrated on figure 2, the sensor is a transducer that converts a force into a resistance variation (the same variation for the two gauges).

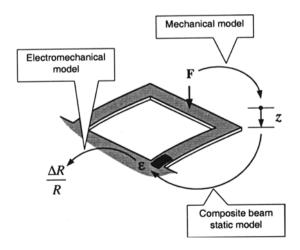


Figure 2: Modeling of the complete transducer.

Three basic models are used in series to mathematically represent this conversion:

- A mechanical model, that describes the relation between the applied force F and the vertical displacement of the cantilever z,
- A structural model for the CMOS beam that takes into account the heterogeneous composition of the structure in order to provide a relation between the vertical displacement z and the strain applied to the gauge,
- And finally, an electromechanical model, that translates the gauge strain into a resistance variation.

Thanks to our previous work on characterization and analytical modeling [4], we have established that the associated mechanical model is a classical second order system. Table 1 reports differential equation and Laplace transfer function where k, M and D represent respectively the stiffness, the mass and the damping factor of the structure:

$M\frac{d^2z}{dt^2} = -k.z - D\frac{dz}{dt} + F$	$z = S_{stat} \times \frac{B}{1 + 2 \cdot \xi \frac{p}{\omega_0} + \frac{p^2}{\omega_0^2}}$	
	(i)	(ii)
	$\omega_0 = \sqrt{\frac{k}{M}}$	$\xi = \frac{D}{2\sqrt{kM}}$
Differential Equation of the mechanical second order system.	Laplace transfer function of the mechanical second order system.(i) cantilever natural pulsation ω_0 .(ii) system damping coefficient ζ	

Here, the vertical displacement is expressed as a function of the magnetic field B and the static sensitivity *Sstat* (μ m/T) given by:

$$S_{stat} = \frac{I \cdot W_c}{k}$$

The structure consists in an heterogeneous stack of various process layers, namely silicon oxides, metals, polysilicon and passivation nitride. In the composite beam model, the width of each layer is normalized regarding its young modulus. This operation transforms the heterogeneous beam into a equivalent homogeneous beam of equivalent Young's modulus E_n , from which we can calculate mechanical parameters such as the moment of inertia I_n and the vertical distance ν between neutral axis and the gauges.

The stiffness k can be calculated from this model:

$$k = \frac{3 \cdot E_n \cdot I_n}{L_c^3}$$

Where Lc is the length of the anchored beams. Using strength of material theory, the strain in the gauge is given by:

$$\varepsilon = \frac{F \cdot L_c}{E_n \cdot I_n / \nu} = \frac{k \cdot z \cdot L_c}{E_n \cdot I_n / \nu} = \frac{3 \cdot z \cdot \nu}{L_c^2}$$

At last, piezoresistivity is a phenomenon that linearly links the strain and the material resitivity. As a result, the electromechanical model is simply:

$$\frac{\Delta R}{R} = Gf \times \varepsilon$$
 Where Gf is called the polysilicon gauge factor.

3. SYSTEM LEVEL INTEGRATION

3.1 Stand-alone cantilever performance

While sensitivity can be useful metric, the real parameter of interest in most application is resolution. We discuss, in this part, about the minimum magnetic induction B that can be measured.

Piezoresistive cantilevers suffer from an unexpected 1/f noise [5]. Unfortunately, 1/f noise in resistors from AMS process are not characterized, and corresponding spice equation is empty. So, in the following, bandwidths avoid low frequencies where 1/f noise can not be neglected.

White noise spectral density is constant over frequency, given for a resistor R about $1.7 k\Omega$ by :

$$V_{noise}^{2}(f) = 4k_{b}TR$$
 $V_{noise,rms}(f) = 5.3 \, \text{nV} / \sqrt{\text{Hz}}$

Where k_b is the Boltzmann's constant, T is the temperature in Kelvin and R is the resistance value in Ohms.

Two constant resistors and two gauges, equal to the same value R, are actually connected together in a Wheatstone bridge circuit that compensates for temperature variation. It gives a total output noise equivalent to the previous single resistor noise level.

The noise spectral density at the output of a magnetic sensor may be interpreted as a result of an noise equivalent magnetic induction (NEMI), acting on a noiseless device [6]. The following curve (figure 3) reports this NEMI spectral density due to White noise in resistors.

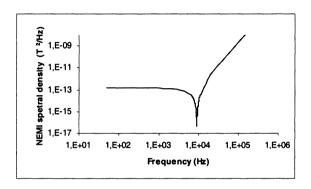


Figure 3: NEMI spectral density due to White noise in Wheatstone bridge.

Our sensor is a electro-mechanical component, thus mechanical noise must be considered. This noise is a thermal noise which can be described with a single relation giving force noise Fm acting on the cantilever:

$$Fm = \sqrt{4k_b TD}$$
 With damping, D=7.19.10⁻⁷ kg.s⁻¹

Then, this force conduces to the NEMI spectral density [6]:

$$S_{B_n/U_{max}}(f) = \frac{4.k_b.T.D}{(W.I)^2}$$

In our application, $S_{Bn/Umec}(f)$ is about 10^{-18} , thus, mechanical noise can be neglected.

3.2 Signal processing

Looking at results of figure 3, it is obvious that the best sensor performances in terms of sensitivity and resolution will be achieved using the resonant mode. The signal is then mechanically filtered and the signal bandwidth is restricted to the -3dB mechanical bandwidth around resonant frequency. This bandwidth is in fact only few hundreds hertz large.

It is a general rule to optimize noise performance that one should not design circuits for larger bandwidth than the signal requires. We thus designed amplification circuit and a filter in order to minimize total noise RMS value accordingly with the already restricted signal bandwidth. A schematic of the integrated circuit is presented on figure 4.

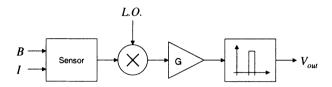


Figure 4: Schematic of the measuring system.

In this circuit, amplification stage and bandpass filter are preceded by a multiplier, which is used to shift the amplifier input signal in the frequency domain. If the cantilever is used in resonant mode, this feature is required for adjustment purpose as long as it is difficult to precisely predict both sensor natural frequency and filter cutoff frequencies. In the case of a static actuation of the cantilever, the multiplier shall be used to shift the signal from DC to higher frequencies where the 1/f noise of amplifier becomes negligible (from the lock-in principle).

Any circuit located before amplification is under major concern concerning noise injection. For this reason, noisy operational amplifiers have been avoided in the design of the mixer. Figure 5 shows a schematic view of the Gilbert's multiplier that we used instead:

Equivalent input noise in this structure strongly depends on the differential pair transistors length. In order to find the optimal length for those transistors, a parametric noise analysis has been performed (figure 6). Because the transistors length also affects the multiplier gain (A decreases when L increases) there is no benefice regarding equivalent input noise in increasing the length too much. Finally, L has been fixed to 5μ m, resulting in a mixer gain A about 10. The layout of the integrated Gilbert's cell is presented on figure 7.

Amplification and filtering use classic, operational amplifier based, circuits. The filter is a second order band-pass filter with a cutoff frequency of 25kHz and a 3dB bandwidth of 3kHz.

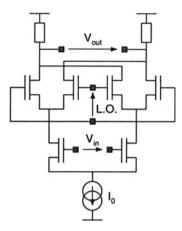


Figure 5: Schematic of the Gilbert's multiplier.

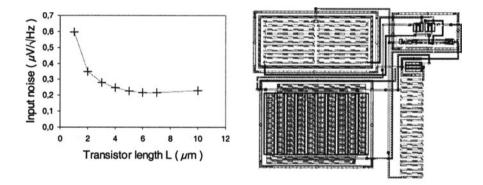


Figure 6: Equivalent input noise as a function of the differential stage transistors length.

Figure 7: Layout of the multiplier

3.3 Simulations

In order to include the mechanical device in the design environment, the complete analytical model of the U-Shaped cantilever has been translated in Analog HDL language that authorizes simulations in a microelectronic designer framework.

This model, which is attached to the mechanical transducer, is used by analog electronic circuits simulation such as Spice. Real numbers are manipulated during the simulation regardless their physical significance. Doing this, a voltage generator can be used to generate any physical stimuli (e.g. a magnetic field) and electrical measurement (voltage or current) might represent any physical information such as displacement or speed.

In figure 8, a magnetic field step from 0 to 1mT is applied to the sensor at t=0. From top to bottom the figure displays the following waveforms: (i) The "force input" which is the voltage source applied across the planar coil, (ii) the local oscillator control signal, (iii) the vertical displacement of the U-Shape in microns, (iv) the amplifier output signal and, (v) the whole system output. Spectrums have been calculated using a time window far enough form the simulation beginning, so that permanent regime as been reached.

In order to shift the 9kHz cantilever resonant frequency to the 25kHz filter central frequency, the local oscillator signal frequency has been fixed at 16kHz. Looking at FFT's the multiplication produces the awaited frequency shift and the filter removes the undesired 6kHz peak.

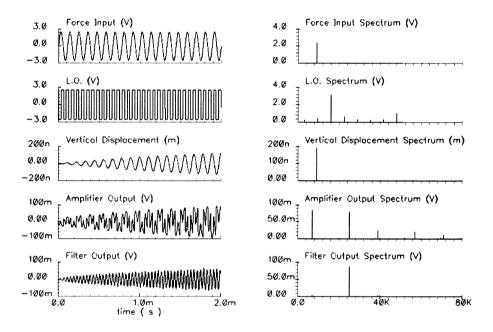


Figure 8: Example of system level simulations.

3.4 Noise performance analysis

Figure 9 shows noise simulation results in terms of noise power spectral density for the complete signal processing circuit including the sensor gauges, the multiplier, the amplifier and the filter. However, as long as nonlinear circuit are not completely supported for noise analysis, the L.O. signal has been fixed to a constant level, transforming the multiplier into a simple amplification stage with a gain of 10. Doing this, the multiplier input noise is not shifted in the frequency domain. This operation does not affect the result

regarding thermal noise into the sensor gauges since its noise power spectral density is constant over the frequency range.

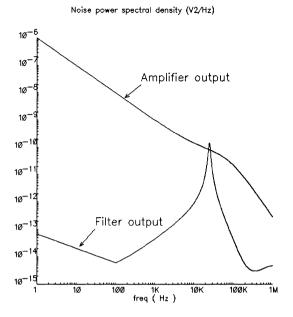


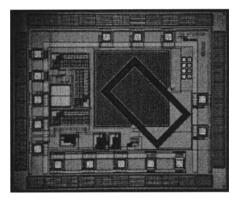
Figure 9: Noise power spectral density before and after filtering

According to simulation results, the total noise level is about 8mVrms before filtering and 820μ Vrms when filtered with a 3kHz bandwidth.

Since total amplification factor with mixer and square local oscillator is 660, the sensitivity of the resonant sensor becomes 350 Vrms/T with a $2 \mu \text{T}$ resolution.

3.5 System fabrication

The system has been fabricated using $0.6\mu m$ CMOS technology from AMS [7]. The figure 10 presents the circuit after CMOS process. The gray square around the U-Shape device is the bare silicon defined during the mask design step by stacking all the oxides openings (diffusion, contact, via and pad). After CMOS fabrication, dies are anisotropically wet etched in a TMAH solution, releasing the cantilever as shown on figure 11.



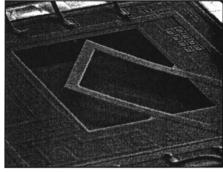


Figure 10: The sensing system after CMOS process.

Figure 11: The sensing system after FSBM post-process.

4. CONCLUSION

An advantage of monolithic integration is the possibility to add electronic functions without significant increase of the total system cost. The U-Shape cantilever performance in the magnetometer application does not reach the performance level of sensors built on dedicated technologies in term of sensitivity. Although, the use of polysilicon resistances gauges in a Wheatstone bridge structure offers many advantages such:

- Effect of temperature on the gauge is cancelled,
- Noise power spectral density in resistances is low and good resolutions are achievable with appropriate signal processing,
- It is easy to interface with electronics.

After amplification and noise filtering, the sensor resolution has been taken to $2\mu T$, giving the system the ability to measure earth magnetic field.

Further improvements of the signal processing circuit will implement oversampling techniques in order to reduce noise level.

5. REFERENCES

- [1] G.Rieger, K. Ludwig, J. Hauch, W. Clemens, "GMR sensors for contactless position detection", Sensors and Actuators A 91 (2001) 7-11.
- [2a] L. Latorre, Y. Bertrand, P. Nouet, "On the use of Test Structures for the Electromechanical characterization of a CMOS compatible MEMS technology", Proc. IEEE 1998 Int. Conf. On Microelectronic Test Structures, March, 1998.
- [2b] L. Latorre, P. Nouet, Y. Bertrand, P. Hazard and F.Pressecq, "Characterization and modeling of a CMOS-compatible MEMS technology", Sensors and Actuators, Vol. 74, 1999, pp. 143-147.
- [2c] L.Latorre, V. Beroulle, Y. Bertrand, I. Salesse, P. Nouet, « Electro-Mechanical Magnetic Field sensors in a CMOS Technology », Proc. of DCIS 2000, pp694-699, 15th Design of Circuits and Integrated Systems Conference, Montpellier, Le Corum, France, November 21-24, 2000.
- [2d] V. Beroulle, Y. Bertrand, L. Latorre, P. Nouet, « Micromachined CMOS Magnetic Field Sensors with Low-Noise Signal Conditioning », 15th IEEE INTERNATIONAL MICRO ELECTRO-MECHANICAL SYSTEMS CONFERENCE, January 20-24, 2002, Las Vegas, Nevada, USA.
- [3] Circuits Multi-projets (CMP) is a broker in Ics, MCMs and MEMS. See http://cmp.imag.fr/
- [4] L. Latorre and al., "Characterization and modeling of a CMOS-compatible MEMS technology", Sensors and Actuators, Vol. 74, 1999, pp. 143-147.
- [5] Jonah A. Harley, "Advances in piezoresistive probes for atomic force microscopy", Dissertation of Ph'D degree, march 2000.
- [6] R. S. Popovic, « Hall effect Devices, Magnetic Sensors and Characterization of semiconductors », The Adam Hilger Series On Sensors, Adam Hilger, ISBN 0-7503-0096-5, 1991
- [7] See http://www.ams.co.at