



Exploration of magnetic memory for ultra low-power systems-on-chip

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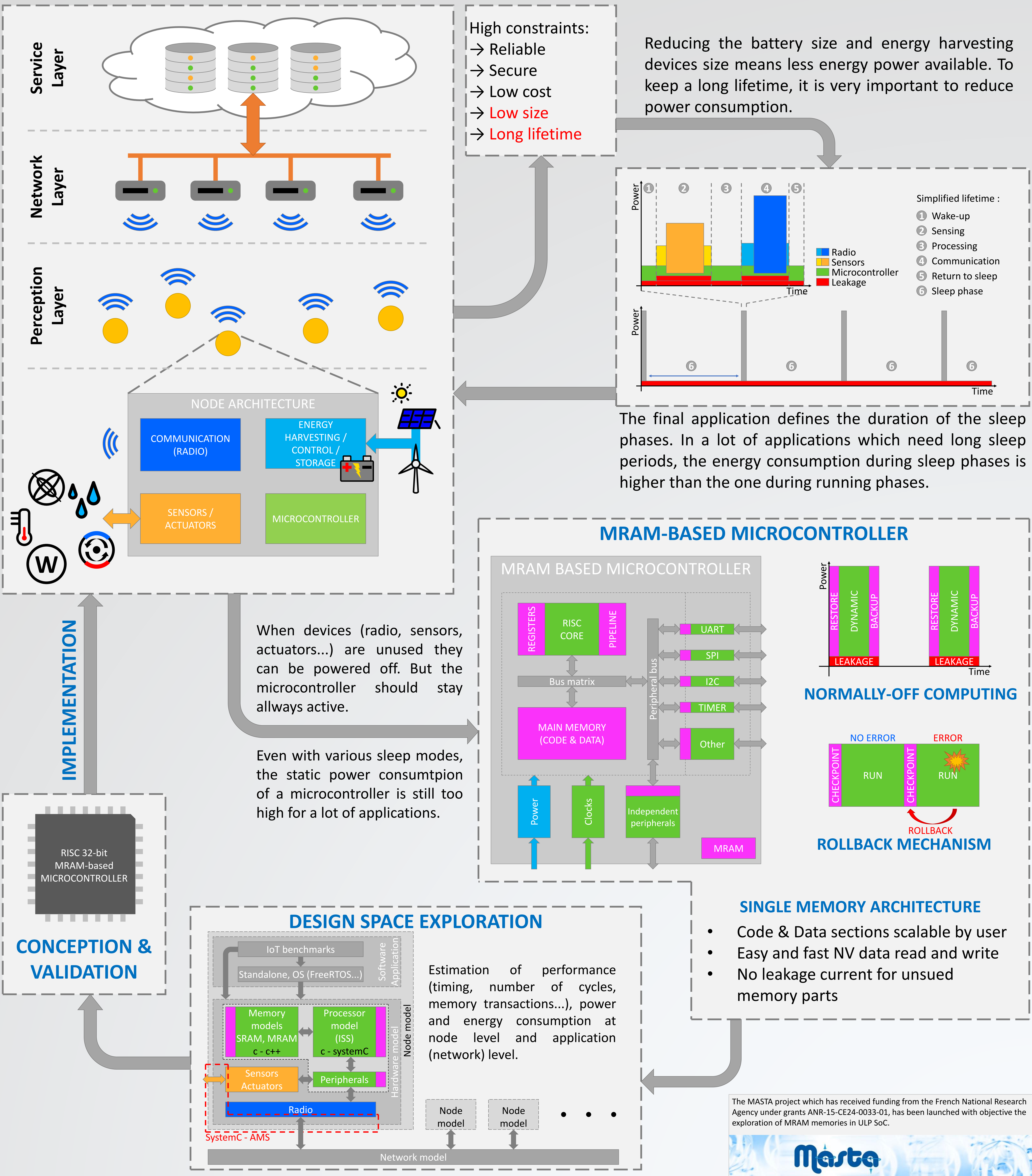
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Using benefits of emerging Non-Volatile memories in ultra low power systems-on-chip to reduce power consumption for Internet of Things devices



The MASTA project which has received funding from the French National Research Agency under grants ANR-15-CE24-0033-01, has been launched with objective the exploration of MRAM memories in ULP SoC.

