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# Wideband Fully Differential Current Driver with Optimized Output Impedance for Bioimpedance Measurements

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**Abstract**—This paper presents a wideband fully differential current driver architecture suitable for bioimpedance measurements. It uses an improved regulated cascode to enhance output impedance, enabling accurate measurements of transfer impedances at low and high frequencies. The current driver architecture maximizes the output voltage swing. An independent reference voltage is used in order to compensate the process variations of the output common mode voltage. The circuit was designed in 0.18 $\mu\text{m}$  CMOS AMS process, operating from 1.8V supply voltage. The silicon area is 0.26 mm<sup>2</sup>. The current driver has a 67 MHz bandwidth and can provide a maximum output current of 600  $\mu\text{A}$  peak with a Total Harmonic Distortion (THD) below 0.3% at low frequencies increasing to 0.6% at 8 MHz. Due to the use of regulated cascodes in the output stage, the circuit achieves a 79M $\Omega$  output impedance at low frequencies decreasing to 324K $\Omega$  at 1MHz, with an output voltage swing of 0.95 V.

**Keywords**—Operational Transconductance Amplifier, bioimpedance, current driver, regulated cascode

## I. INTRODUCTION

Bioimpedance spectroscopy (BIS) is a technique increasingly used for measuring the electrical properties of biological tissues [1]. Since bioimpedance is a function of the physiological processes, variations of the composition of biological tissues can be seen as variations of the electrical impedance of the tissue.

In order to measure bioimpedance, current stimulation is generally used. Thus, a current containing a single or multiple frequencies is injected in the tissue under test via electrodes, the bio-modulated response voltage is then measured to determine the impedance transfer function.

For a rigorous characterization of the biological tissue composition, the complex impedance needs to be analyzed over a large range of frequencies. In fact, each tissue molecule has a different response to a given frequency. Indeed the electrical permittivity of biological tissues decreases in three main steps corresponding to three dispersions [2] : the  $\alpha$  dispersion (10 Hz to 10 KHz), the  $\beta$  dispersion (10 KHz to 10 MHz) and the  $\gamma$  dispersion ( $\geq 10$  MHz). Since  $\alpha$  and  $\beta$  dispersions are associated with the most relevant biological aspects [3], the current should have a constant amplitude over these frequency ranges. This is achieved by having a large

output impedance compared to the load. In fact, at lower frequencies, the sum of the electrodes-interface and tissue impedances have the highest magnitudes. Whereas at higher frequencies, stray capacitance represents the biggest challenge since it shunts the output resistance of the current driver which reduces its value.

Due to low voltage supply specifications and the need for high output current amplitudes necessary to ease the response analysis, the output voltage swing is a critical parameter for the current driver. Also, in implanted measurements applications, the electrode-interface impedance may increase due to biological effects such as fibrosis. Thus, care must be taken to ensure a maximal output voltage swing.

The Howland current driver is widely used in bioimpedance applications. However, the output impedance and stability of the Howland source are dependant of the accurate matching of the resistors in the positive and negative feedback [4], which makes it more suitable for discrete designs.

An alternative option is to use Operational Transconductance Amplifier (OTA) based current drivers, which are well suited for applications requiring a fully integrated circuit.

The integrated current driver needs to have a large bandwidth to cover at least the first two frequency dispersions. The output impedance should be maximized especially in low frequencies in order to be insensitive to the variation of the relatively high electrode-tissue interface impedances.

A current driver architecture providing high output impedance has been introduced in [5], an improved version of this topology has been described in [6]. It is capable of providing high output impedance using negative feedback that senses and regulates the output current. However, the design is complex and the output impedance is limited to 665 K $\Omega$  at 100 KHz decreasing to 62 K $\Omega$  at 1 MHz.

This paper presents the design and simulation of a current driver architecture providing high output impedance at  $\alpha$  and  $\beta$  frequency ranges. The output voltage swing is maximized making it suitable for integrated designs operating from a low supply voltage. Section II presents the current driver topology as well as the design methodology. Simulation results and comparison with existing architectures are presented in Section III. Finally Section IV concludes the paper.

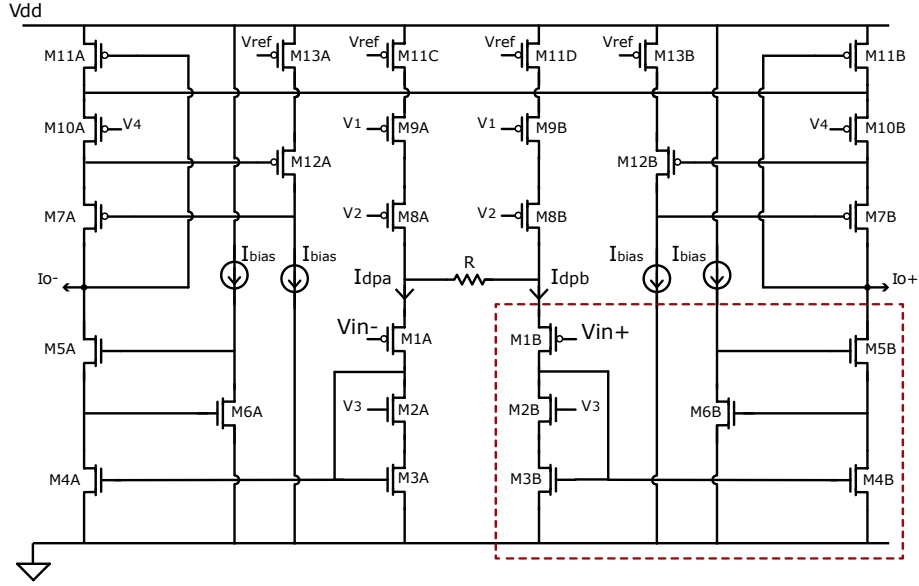


Fig. 1. Current driver architecture

## II. CURRENT DRIVER'S ARCHITECTURE OPTIMIZATION FOR BIOIMPEDANCE APPLICATIONS

### A. Global architecture/Topology

Fig.1 shows the architecture of the current driver. It is a symmetrical fully-differential OTA. The first stage of the OTA consists of a degenerated differential pair. A degeneration resistance was added to the M1 transistors in order to enhance the linearity of the transfer function and to maximize the range of input voltage. The current across the differential pair branches has been set in order to have a bandwidth of operation covering the  $\alpha$  and  $\beta$  frequency dispersions.

The output stage is a critical part of the current driver structure. It needs to provide a high output impedance while enabling a maximum output swing. Using regulated cascodes current mirror as the output stage architecture provides the best compromise between a high output resistance and the lowest output voltage.

Since the OTA is fully-differential, a common-mode feedback (CMFB) loop is implemented to stabilize the common-mode output voltage at half supply [7]. Due to low supply voltage specifications and in order to compensate for process variations, the output DC level is tunable using an independent reference voltage (Vref).

### B. Output impedance increasing

The output stage of the current driver is critical. It has to provide an output current proportional to the input current at the high impedance output node. The output current amplitude needs to be constant regardless of loading. The important factors defining the performance of the output stage are thus output impedance, minimum output voltage and accuracy.

Bandwidth specifications imposed by the application (bioimpedance spectroscopy) require the use of short channel MOSFETs. A size reduction of the transistors leads to a decrease in the output resistance. Simple current mirror

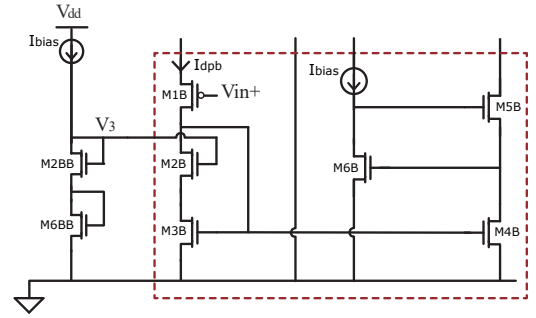


Fig. 2. Improved regulated cascode architecture

blocks or basic cascode structures are unable to provide an acceptable output resistance, especially at low frequencies where the load (electrode-tissue interface impedance) displays the highest impedance magnitudes, generating measurements errors in that frequency range. Therefore, improved output stage architectures for higher performances should be used.

Regulated cascode current mirror have the highest output impedance compared to simple or Wilson cascodes. It uses negative feedback in order to enhance the output impedance.

The improved regulated cascode shown in Fig.2 is the basic block of the output stage. In order to minimize loading effects, the transistor  $M_{6B}$  is used to enhance the output impedance by a  $gm \cdot ro$  factor compared to basic cascode structures. The output impedance of the improved regulated cascode can be written as :

$$R_{out} = ro_{M4} * ro_{M6} * gm_{M6} * ro_{M5} * gm_{M5} \quad (1)$$

The main drawback of the regulated cascode is the output minimum voltage, which is equal to  $V_{th} + 2V_{eff}$ . This could be improved to around  $2V_{eff}$  by using the  $M_6$  transistors in weak inversion. Although the matching of the  $M_6$  transistors

will be affected when operating in weak inversion, the loop gain of the structure is so important that the matching errors are negligible.

In order to enhance the accuracy of the current mirror, the drain to source voltages of  $M_{3B}$  and  $M_{4B}$  should be equalized. Since the  $V_{ds}$  of  $M_{4B}$  is imposed by the gate voltage of  $M_{6B}$ , the proposed solution consists in using in a mesh an  $M_{6BB}$  transistor of similar size as  $M_{6B}$  biased with the same current, together with an  $M_{2BB}$  transistor of similar  $V_{gs}$  as  $M_{2B}$  :

$$V_{dsM_{4B}} = V_{gsM_{6B}} \quad (2)$$

$$V_{gsM_{6B}} = V_{gsM_{6BB}} \quad (3)$$

$$V_{dsM_{3B}} = V_{gsM_{2BB}} - V_{gsM_{2B}} + V_{gsM_{6BB}} \quad (4)$$

$$V_{dsM_{3B}} = V_{gsM_{6BB}} = V_{dsM_{4B}} \quad (5)$$

### C. Output voltage swing maximization

Due to the low voltage supply specifications and the need for high output current amplitudes in order to ease the response analysis, the output voltage swing is a critical parameter of the current driver. Also, in implanted measurements applications, the electrode-tissue interface impedance may increase over time due to biological effects such as fibrosis. To avoid saturation errors, the output voltage swing should be maximized.

In order to maximize the output swing voltage, the architecture of the OTA should be carefully chosen. The symmetrical architecture offers the highest output swing voltage compared to the folded cascode or the telescopic topologies [7]. OTA based current drivers could be single-ended or fully-differential. Fully-differential current driver have twice the output voltage swing compared to the single-ended architectures, which has the advantage to allow for doubling the range of impedances that can be measured.

The common-mode feedback is implemented using  $M_{11}$  transistors as follows: output voltages are measured by  $M_{11A}$  and  $M_{11B}$  transistors, then the drains of the  $M_{11A}$  and  $M_{11B}$  transistors are connected to cancel the differential signal and the loop is closed. The gate voltages of  $M_{11C}$  and  $M_{11D}$  are controlled by an independent reference voltage. If the gate voltages of  $M_{11C}$  and  $M_{11D}$  are set to the half supply voltage, by matching  $M_{11A} - M_{11B}$  to  $M_{11C} - M_{11D}$  the output voltages are set to half supply voltage.

Another solution consists in only using  $M_{11A}/M_{11B}$  transistors in their triode region without using a reference voltage as in [6]. However this solution is subject to process variations. In low supply voltage driven designs, the output common mode voltage should be accurately set to half supply voltage. In order to compensate for process variations, the  $M_{11C}/M_{11D}$  gates are driven by an adjustable reference voltage that consists in a multi-level digitally controlled voltage divider.

## III. RESULTS

The current driver was designed in a 0.18  $\mu\text{m}$  AMS CMOS process operating at 1.8 V power supply. The current driver is capable of providing currents up to 600  $\mu\text{A}$  peak to peak. Fig.6 shows the layout of the circuit, the current driver occupies an area of 0.26  $\text{mm}^2$ . The circuit design, simulations and layout

were developed with Cadence suite using the toolkit provided by the foundry.

The Frequency response of the current driver transconductance is presented in Fig.3. It has a transconductance of 860  $\mu\text{S}$  up to 4 MHz decreasing to 854  $\mu\text{S}$  at 8 MHz. The cutting frequency is located at 67 MHz. The current driver was loaded with a 1 k $\Omega$  resistor with only its intrinsic stray capacitance.

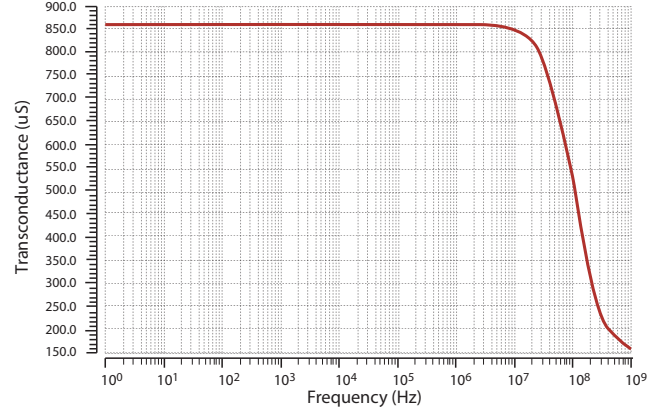


Fig. 3. Current driver bandwidth

Total Harmonic Distortion (THD) was simulated for a 400  $\mu\text{A}$  peak to peak output current. The simulation results have showed a THD below 0.3% at low frequencies increasing to 0.6% at 8 MHz.

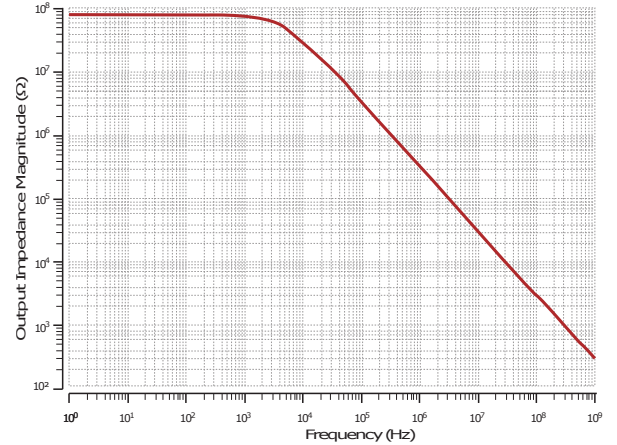


Fig. 4. Output impedance of the current driver

### A. Output impedance

The frequency response of the output impedance of the current driver is presented in Fig.4. The output impedance is equal to 79 M $\Omega$  at DC up to 1 KHz. It decreases to 3.6 M $\Omega$  at 100 KHz, and to 324 k $\Omega$  at 1 MHz. The low frequency output impedance magnitudes are suitable for exploring the physiological processes occurring in the  $\alpha$  region. At higher frequencies, the output impedance values are several orders of magnitude higher than the electrode-tissue impedance, since it is expected to decrease in the  $\beta$  region. A research presented in [11] shows a comparison of the impedance magnitudes of several Ag/AgCl commercial electrodes in the frequency

TABLE I. RESULTS COMPARISON WITH EXISTING ARCHITECTURES

Architectures	[8]	[9]	[10]	[6]	Our work
Bandwidth	1KHz - 1MHz	10KHz-250KHz	100Hz-100KHz	>500KHz	10Hz-10MHz
Output impedance	670k $\Omega$ @ 100kHz 330k $\Omega$ @ 300KHz	149k $\Omega$ @ 100KHz 30k $\Omega$ @ 500KHz	100k $\Omega$ @ 100KHz	665 k $\Omega$ @ 100kHz 372 k $\Omega$ @ 500kHz 64 k $\Omega$ @ 1MHz	79M $\Omega$ @ 100Hz 3.24M $\Omega$ @ 100KHz 324k $\Omega$ @ 1MHz
THD	-	0.45% @ 1 mA <sub>pp</sub>	0.2% @ 200 $\mu$ A <sub>pp</sub>	0.53% @ 2 mA <sub>pp</sub>	0.3% @ 400 $\mu$ A <sub>pp</sub>
Output swing	-	-	0.4V	15V	0.95V
Supply voltage	-	30V	1.8V	18V	1.8V

range (10 Hz-1 MHz), The average electrode-tissue impedance magnitudes were higher than 10 K $\Omega$  for frequencies below 10 KHz, equal to 5 K $\Omega$  at 10 KHz and 337  $\Omega$  at 1 MHz. The current driver output impedance results compared to the electrode-tissue magnitudes presented in [11] show that it is capable of providing measurements with an error of 0.02% at 10 KHz and 0.1% at 1 MHz.

### B. Output voltage swing

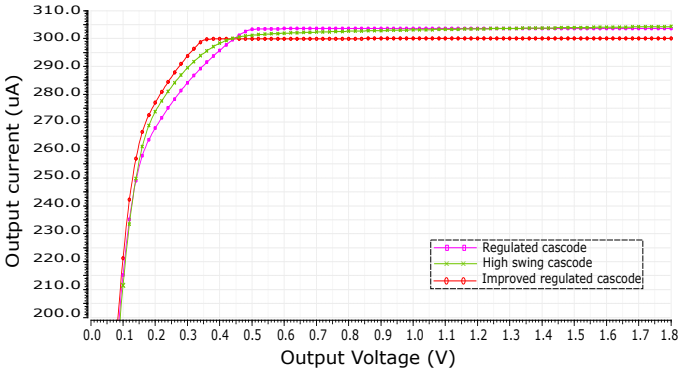


Fig. 5. Output current vs output voltage characteristic of the high swing cascode, the regulated cascode and the improved regulated cascode for an input current of 300  $\mu$ A

Fig.5 shows the output current versus the output voltage characteristic of the NMOS improved regulated cascode presented in section II compared with the characteristics of the high swing cascode and the classic regulated cascode. For the high swing cascode, output resistance is minimal. While the classic regulated cascode provides high output resistance, the output current is not accurate, and the output minimum voltage is maximal (around 0.55 V). The improved regulated cascode presented in section II provides high output impedance, high accuracy and low minimum output voltage (0.4 V).

The common mode transistors ( $M_{11}$ ) were designed for a  $V_{eff}$  of 50 mV. This value is a compromise between the overall output swing and the variability of the common mode feedback transistors. A Montecarlo simulation (process and mismatch) of the DC output level showed a mean value of 901 mV with a standard deviation of 9.91 mV.

Therefore, the overall output voltage swing is around 0.95 V from a supply voltage of 1.8 V.

### C. Comparison table with existing architectures

Table I presents a performance comparison with existing discrete design current driver architectures [8] [9] as well as

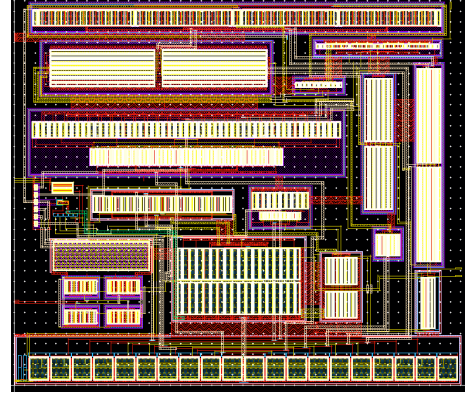


Fig. 6. Layout of the current source

integrated current driver architectures [10][6] for bioimpedance measurement application. However, only simulation results are available for the proposed architecture whereas the cited architectures present measurement results. For a fair comparison with the integrated current driver architectures, the output impedance magnitudes are simulated for a 400  $\mu$ A peak to peak output current as in [6] and [10]. Regarding the constraints induced by the need for bioimpedance measurement over a wide frequency range, the comparison table shows that our proposed architecture provides the highest output impedance magnitudes over the widest frequency range.

## IV. CONCLUSION

A current driver architecture optimized for bioimpedance measurements has been presented. It uses an improved regulated cascode to enhance the output impedance, enabling accurate measurements of transfer impedances at low and high frequencies. The current driver uses a common-mode feedback compensation technique capable of accurately setting the output common mode voltage independently of process variations. The proposed architecture offers a high output swing for a given supply voltage. A circuit implementing the design methodology was designed and simulated in a 0.18 $\mu$ m CMOS AMS process. It occupies an area of 0.26 mm<sup>2</sup>.

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