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A Multitone Analysis for Bioimpedance Spectroscopy using Minimal Digital Ressource

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Abstract—Bioimpedance spectroscopy consists in measuring the complex impedance of biological tissues over a large frequency domain. This method is convenient in particular for studying body composition, blood characterization and even cancer detection. This wide range of applications makes it suitable as a part of health monitoring systems. Today’s self-monitoring devices tend to be portable, wearable or even implantable. Next generation bioimpedance sensing systems thus require to be implemented with power and resource savings in mind.

Impedance measurement methods are divided into two main categories. Some are based on “single-tone” signals while the others use “multi-tone” signals. First methods use a pure frequency sine wave to make the measurement. They benefit from a very simple analysis that may consist in synchronous demodulation. However, the operation must be repeated for each frequency over the domain of interest. Due to this necessary frequency sweep, the total measurement may take long. On the other hand, generating a multi-frequency signal allows the analysis to cover the whole frequency range simultaneously. This is at the cost of a more complex analysis algorithm (discrete cosine transform – DCT, typically). Unfortunately both methods result in excess power consumption: long time of measurement for single-tone frequency sweep, hardware and computational resources for multi-tone. That make both approaches hardly suitable for embedded applications.

In this paper, we propose an intermediate approach that combines the speed of multi-tone systems with a much simpler analysis algorithm than DCT or FFT. Using specific properties of the bioimpedance, we even show that we can get rid of any multiplier. This results in a minimal implementation using only adders and synchronous ADC. For optimal performances, this small footprint digital processing can be easily synthesized and embedded on a mixed-mode ASIC together with the analog frontend.

Index Terms—Bioimpedance spectroscopy, multi-frequency, digital processing

I. INTRODUCTION

Bioimpedance spectroscopy consists in measuring the complex impedance of biological tissues over a large frequency domain [1]. This method is convenient in particular for studying body composition [2], blood characterization [3] and even cancer detection [4]. This wide range of applications makes it suitable as a part of health monitoring systems. Today’s self-monitoring devices tend to be portable, wearable or even implantable. Next generation bioimpedance sensing systems thus require to be implemented with power and resource savings in mind.

Impedance measurement methods are divided into two main categories. Some are based on “single-tone” signals while the others use “multi-tone” signals. First methods use a pure frequency sine wave to make the measurement [5], [6], [7]. They benefit from a very simple analysis that can consist in synchronous demodulation. However, the operation must be repeated for each frequency over the domain of interest. Due to this necessary frequency sweep, the total measurement may take long. On the other hand, generating a multi-frequency signal allows the analysis to cover the whole frequency range simultaneously [8], [9], [10]. This is at the cost of a more complex analysis algorithm (discrete cosine transform – DCT, typically). Unfortunately both methods result in excess power consumption: long time of measurement for single-tone frequency sweep, hardware and computational resources for multi-tone. That make both approaches hardly suitable for embedded applications.

In this paper, we propose an intermediate approach that combines the speed of multitone measurement with a much simpler analysis algorithm than DCT or FFT. First, we consider some a priori facts about bioimpedance. Those allow us to simplify the resolution needs for a bioimpedance-specific sensor compared to a generic purpose impedance analyzer. Based on this frequency distribution, we propose in the third section a simplified analysis process based on synchronous sampling demodulation. The fourth section gives the proposal of a digital implementation of the algorithm using only adders and frequency dividers. The last section opens some discussions about the limits of the proposed approach.
constant phase elements of impedance

\[ Z_{\text{CPE}}(f) = \frac{1}{q_0(2\pi f)^\alpha}, \quad q_0 \text{ arbitrary parameter, } 0 < \alpha < 1. \]  

Identifying such elements is better achieved with logarithmically growing frequencies. Designing a bioimpedance-specific sensor, we can adopt a frequency distribution of the form

\[ f_i = \frac{f_0}{2^i}, \quad 0 \leq i < n_t, \]  

for the multitone excitation signal. This gives \( n_t \) frequencies with a maximum of \( f_0 \) and a resolution of \( \log_2(10) \approx 3.32 \) points per decade. This is expected to be enough to extract characteristics of quite smooth frequency responses.

### III. IMPEDANCE ESTIMATION

The chosen principle of bioimpedance sensing is illustrated Fig. 2. With the above considerations, generated current \( i(t) \) can be expressed as the superposition of \( n_t \) sine waves. Current being real, hermitian symmetry applies to complex amplitudes \( (I_{-i} = I_i^*) \) such that

\[ i(t) = \sum_{i=0}^{n_t-1} I_i e^{2\pi i f_i t} + I_i^* e^{-2\pi i f_i t}. \]

For the sake of simplicity, we can let all \( I_i \) to be equal to \( \frac{1}{n_t} \) (normalized amplitudes and null phase for all frequency components). If not so, we can get back to this case by resistor calibration.

\[ i(t) = \frac{1}{2} \sum_{i=0}^{n_t-1} e^{2\pi i f_i t} + e^{-2\pi i f_i t}. \]

Let us express the Fourier transform \( I(f) = \mathcal{F}\{i(t)\} \) in the frequency domain

\[ I(f) = \frac{1}{2} \sum_{i=0}^{n_t-1} \delta(f - \frac{f_0}{2^i}) + \delta(f + \frac{f_0}{2^i}), \]  

with \( \delta(t) \) denoting the Dirac distribution. We can derive from the last equation the voltage expression \( V(f) = \mathcal{F}\{v(t)\} \) in the sinusoidal steady state.

\[ V(f) = Z(f)I(f) \]

\[ = \frac{1}{2} \sum_{i=0}^{n_t-1} Z_i \delta(f - \frac{f_0}{2^i}) + Z_i^* \delta(f + \frac{f_0}{2^i}), \]  

with \( Z_i = Z(f_i) \) and \( Z_i^* = Z(f_{-i}). \)

The objective of the following processing is to extract the values \( Z_i = Z(f_i) \) from the acquired \( v(t) \) signal. The main idea is to use synchronous sampling demodulation. Indeed, \( Z(f_0) \) can be obtained by sampling \( v(t) \) at the higher frequency \( f_0 \) and by low-pass filtering (averaging) the resulting samples. However, sampling the signal at lower frequencies results in aliasing. Let us explicitly express the result of sampling \( v(t) \) at \( f_j = \frac{f_0}{2^j} \). In the frequency domain, sampling is equivalent to convolution product by a Dirac comb.

\[ V(f) * \sum_{k \in Z} \delta(f - \frac{k f_0}{2^j}) = \]

\[ \frac{1}{2} \sum_{k \in Z} \sum_{i=0}^{n_t-1} Z_i \delta(f - f_0(\frac{k}{2^j} + \frac{1}{3})) + Z_i^* \delta(f - f_0(\frac{k}{2^j} - \frac{1}{3})). \]

We isolate from the last equation the DC component \( S_j, i.e. \) the coefficient of \( \delta(f) \). For all \( i \) between 0 and \( j \), there are two integers \( k = \pm 2^j - i \) that satisfy

\[ \frac{k}{2^j} \pm \frac{k}{2} = 0. \]

Summing all contributions, we get

\[ S_j = \sum_{i=0}^{j} (Z_i + Z_i^*) = \sum_{i=0}^{j} \Re(Z_i). \]

In the same manner, we can get the quadrature components by delaying the sample time by a quarter of period \( \frac{f_0}{4}. \)

\[ V(f) * \sum_{k \in Z} \delta(f - \frac{k f_0}{2^j}) e^{-2\pi f \frac{j - i}{4 f_0}} = \]

\[ \frac{1}{2} \sum_{k \in Z} \sum_{i=0}^{n_t-1} Z_i \delta(f - f_0(\frac{k}{2^j} + \frac{1}{3})) e^{-i \frac{k}{2}} + Z_i^* \delta(f - f_0(\frac{k}{2^j} - \frac{1}{3})) e^{-i \frac{k}{2}}. \]

Again, for \( k = \pm 2^j - i \) we get the DC component

\[ Q_j = \sum_{i=0}^{j} Z_i e^{-i \frac{2^j - i - 1}{2}} + Z_i^* e^{-i \frac{2^j - i - 1}{2}}. \]
Thus, \( f_{\text{out}} \) can be easily done by averaging the samples over a period of the whole multitone signal. Then, sample decimation is performed between each stage by dividing the frequency by two (Fig. 3).

Practically, we These functions are grouped in a block called Demod, for demodulator, that can be easily instantiated from a generic description (Fig. 4). More in details, at each stage, we generate two clock signals in quadrature (Fig. 5). They control two identical accumulators (Fig. 6). Samples are accumulated at the rate corresponding to a particular frequency \( f_i \). The adder size is adapted to avoid overflow. Then dividing by \( \frac{n}{f_f} \) is done by keeping only the \( n_b \) most significant bits. So, all \( S_j \) and \( Q_j \) accumulation are eventually encoded with the same precision.

Values of \( S_j \) (in-phase) and \( Q_j \) (in-quadrature) are saved in the output register at the end of the global period \( \frac{2^n}{f_0} \). This is achieved using a simple frequency divider on the clock output of the last demodulator. We choose not to embed the computation of actual real and imaginary parts of the impedance as described by (15) to (17). It can be done without transmission penalty (same size of data) as a post-process. The objective is to keep the digital circuit as simple as possible in low-power constraint.

This parallel architecture is easily scalable. Moreover each stage only uses two adders together with a few flip-flops to divide the frequency. For optimal performances, it is totally realistic to embed this digital processing directly with the analog front-end (current source and sensing amplifier) in a mixed-mode ASIC.

If \( j \geq 2 \), we can isolate the terms corresponding to \( i = j \) and \( i = j - 1 \).

\[
Q_j = \frac{1}{2^n} \sum_{i=0}^{j-2} Z_i e^{j2\pi (j-i)} + Z^*_i e^{-j2\pi (j-i)} + \frac{1}{2} (Z_{j-1} e^{j\pi} + Z^*_j e^{-j\pi}) + \frac{1}{2} (iZ_j - iZ^*_j) \tag{13}
\]

Thus,

\[
Q_j = \sum_{i=0}^{j-2} \Re(Z_i) - \Re(Z_{j-1}) - \Im(Z_j) \tag{14}
\]

From (10), (12) and (14), we can recursively calculate real and imaginary parts of \( Z_j \).

\[
\Re(Z_0) = S_0, \quad \Im(Z_0) = -Q_0, \tag{15}
\]

\[
\Re(Z_1) = S_1 - S_0, \quad \Im(Z_1) = -Q_1 - S_0, \tag{16}
\]

\[
\Re(Z_j) = S_j - S_{j-1}, \quad \Im(Z_j) = 2S_{j-2} - S_{j-1} - Q_j \tag{17}
\]

**IV. IMPLEMENTATION**

The main advantage of the proposed method is the simplicity of its digital implementation. Like classical synchronous demodulation, it only requires an ADC sharing the same clock as the signal generation (Fig. 2). Samples are fed by the ADC at the rate of \( f_{\text{ADC}} = 4f_0 \). Then, they are re-sampled at each frequency \( f_i \), composing the multitone signal. The extraction of the DC component can be easily done by averaging the samples over a period of the whole multitone signal. Then, sample decimation is performed between each stage by dividing the frequency by two (Fig. 3).

Practically, we These functions are grouped in a block called Demod, for demodulator, that can be easily instantiated from a generic description (Fig. 4). More in details, at each stage, we generate two clock signals in quadrature (Fig. 5). They control two identical accumulators (Fig. 6). Samples are accumulated at the rate corresponding to a particular frequency \( f_i \). The adder size is adapted to avoid overflow. Then dividing by \( \frac{n}{f_f} \) is done by keeping only the \( n_b \) most significant bits. So, all \( S_j \) and \( Q_j \) accumulation are eventually encoded with the same precision.

Values of \( S_j \) (in-phase) and \( Q_j \) (in-quadrature) are saved in the output register at the end of the global period \( \frac{2^n}{f_0} \). This is achieved using a simple frequency divider on the clock output of the last demodulator. We choose not to embed the computation of actual real and imaginary parts of the impedance as described by (15) to (17). It can be done without transmission penalty (same size of data) as a post-process. The objective is to keep the digital circuit as simple as possible in low-power constraint.

This parallel architecture is easily scalable. Moreover each stage only uses two adders together with a few flip-flops to divide the frequency. For optimal performances, it is totally realistic to embed this digital processing directly with the analog front-end (current source and sensing amplifier) in a mixed-mode ASIC.
The proposed architecture may rise several questions. The main one should be the effect of noise on the measurement. We did not yet study in details the aliasing of white noise over the stages of the structure. In particular, low frequencies suffer from averaging low numbers of samples. The worst occurring for \( f_{n1-1} = \frac{f_0}{2} \) where only two samples are used to calculate \( S_t \) and \( Q_t \). However, we can observe that low frequencies have the benefit of best SNR while the acquisition, when using current generator and voltage sensor. This is due to the monotonically decreasing typical behavior of bioimpedances (see Fig. 1). For high frequencies where the SNR worsens, the averaging effect enhances the expected precision. Also, another possibility may obviously be the adaptation of current generation with increased amplitudes at high frequencies.

Another concern may be the very-low frequency noise due to amplifier offset, electronics \( 1/f \) noise, electrode interface potential, etc. It would induce DC component that would impact all \( S_j \) and \( Q_j \) values. However, it appears relatively easy to get rid of this effect by averaging all samples at \( f_{clk} \), i.e. before decimation and subtracting this DC value during post-process.

The last issue could be the fixed resolution due to frequency distribution as powers of \( \frac{1}{2} \). If more resolution is needed, a non-power-of-two frequency division can be introduced, e.g. \( \frac{f_{clk}}{4} \) and then used instead of \( f_{clk} \) in a duplicated structure. Of course, expressions of \( S_j \) and \( Q_j \) will be affected and need to be re-evaluated. However, only post-processing computation will be modified.

VI. Conclusion

We have proposed a very efficient implementation of multitone analysis for bioimpedance sensing. Thanks to general characteristics of bioimpedance, we have chosen a logarithmic frequency distribution. With this hypothesis, we can explicit the aliasing resulting from successive synchronous demodulations and decimations. This expression can be used to post-process the measurements and estimate real and imaginary parts of the impedance for each frequency. The digital implementation consists only in accumulators and frequency dividers and does not make any use of multipliers. Moreover, the structure can be easily scaled to wider frequency ranges.

REFERENCES