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Heavy Ion Induced Degradation in SiC Schottky Diodes: Incident Angle and Energy Deposition Dependence

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Abstract – Heavy-ion induced degradation in the reverse leakage current of SiC Schottky power diodes exhibits a strong dependence on the ion angle of incidence. This effect is studied experimentally for several different bias voltages applied during heavy-ion exposure. Additionally, TCAD simulations are used to give insight on the physical mechanisms involved.

Index Terms – Ion radiation effects, Modeling, Power semiconductor devices, Schottky diodes, Silicon carbide

I. INTRODUCTION

SILICON CARBIDE (SiC) has very good properties for use in power device applications. In comparison to silicon, it has higher breakdown field and higher thermal conductivity. However, the sensitivity of SiC power devices (MOSFETs and diodes) to particle radiation has

been found to be surprisingly high [1]–[3]. Moreover, the radiation response of SiC Schottky devices to heavy ions has been shown to differ from that of silicon-based power devices. The silicon power devices typically exhibit separate regions of destructive and non-destructive response as a function of voltage [4], [5]. In addition to these two regions, SiC Schottky devices have been reported to exhibit also a third region where devices suffer from gradual degradation under heavy-ion exposure [2], [6], [7]. This gradual degradation complicates the assessment of SiC parts for catastrophic failures like Single Event Burnout (SEB). Typically, these parts are tested by using heavy-ion beams perpendicular to the device surface. For most silicon power devices, this is considered to be the worst-case exposure for destructive failures, depending on the actual device geometry [8].

In this paper, we show that a heavy-ion beam at normal incidence is also the worst case for SiC Schottky power diodes. The degradation rates exhibit significant reduction with increasing tilting angle for these devices at constant reverse-bias. The behavior does not follow any typical cosine-laws. The observed degradation has been attributed to thermal effects that are initiated by complex interactions between the reverse-bias applied to the diode and the energy deposited by the incident heavy-ions [9], [10].

In this work, TCAD simulations have been performed to study the thermo-electric response of SiC Schottky structure to heavy ions. The simulation results are consistent with the explanation that the differences in the degradation rates observed at different tilting angles are due to different thermal responses. A simplified explanation is that the misalignment between the ion-induced charge filament and the potential gradient

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within the diode depletion region reduces the density of the collected charge within the Schottky structure, hence the degradation.

This paper builds on the work presented at the RADECS 2016 conference and partly reported in [11].

II. EXPERIMENTAL METHODS

The devices used in this study were commercial SiC Schottky power diodes, manufactured by STMicroelectronics. The device types were STPSC1006D (600V, 10A) and STPSC10H065DY (650V, 10A). The first device type is based on a plain Schottky junction structure, and the latter is a junction barrier Schottky (JBS) diode. The epilayer thickness in both diodes is about $6\mu\text{m}$.

The plastic packaging material was removed from the diodes prior to irradiation to enable sufficient penetration for the heavy ions inside the device, i.e., beyond the Schottky junction and the epitaxial layer. The total thickness of overlying passivation layers and the epitaxial layer was estimated to be on the order of ten micrometers.

The heavy-ion irradiations for this work were performed at the RADiation Effects Facility (RADEF) in the Accelerator Laboratory of the University of Jyväskylä [12]. This paper presents results obtained for Xe ions with energy of 1217 MeV. The linear energy transfer (LET) value for these ions is $62\text{ MeV}/(\text{mg}/\text{cm}^2)$ and the average projected range in SiC is $60\mu\text{m}$, as estimated by using the code from [13]. All the irradiations and the device characterization were performed at ambient temperature in vacuum conditions. The incident angle of the ion beams relative to normal was varied between 0 and 45 degrees. The ion flux ranged between 20 and $6 \cdot 10^5\text{ cm}^{-2} \cdot \text{s}^{-1}$ and cumulative fluences between the irradiation runs varied from 10^3 to 10^6 cm^{-2} .

The current-voltage characteristics for the devices were measured both for the forward and the reverse voltages, before and after each irradiation run. During the irradiations, the device under test (DUT) was biased at fixed reverse voltage and the leakage current was monitored. The device biasing and characterization was done using a Keithley 2410 Source Measure Unit [14]. The voltage limit for the 2410 is 1100V. Because the DUTs were not coated, the maximum voltage levels during testing were limited to about -350 V due to arcing that occurs approximately at -400 V .

III. EXPERIMENTAL RESULTS

Heavy-ion exposure has been shown to increase

reverse leakage current in SiC Schottky diodes, when a sufficient reverse bias is applied during radiation exposure [6], [7], [15]. When an ion beam hits the device at an angle off normal incidence, a similar, but weaker, effect is observed [11]. This is illustrated in Figure 1, where the evolution of leakage current for an STPSC10H065DY diode during Xe-ion exposure is given for three different biasing configurations, while the device was tilted at 20° off from normal incidence.

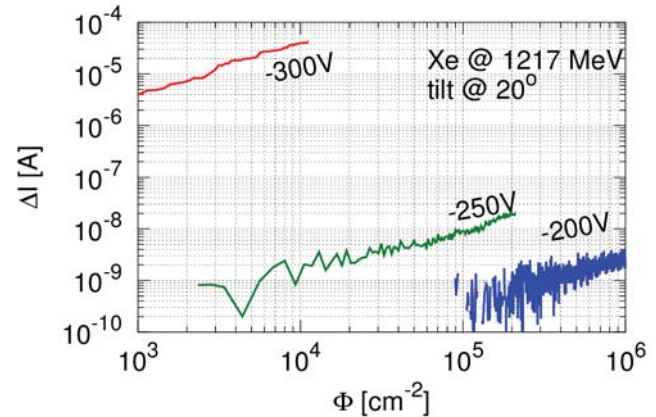


Figure 1. Change in leakage current for STPSC10H065DY diode as a function of cumulative Xe ion fluence at 20° tilting angle for three different biasing configurations. All data are obtained for a single device.

The linear dependence of leakage current on the ion fluence is clearly visible. The rate of degradation is defined by the slope of these data curves. Both device types used in this work exhibited similar responses to the Xe-ion exposure.

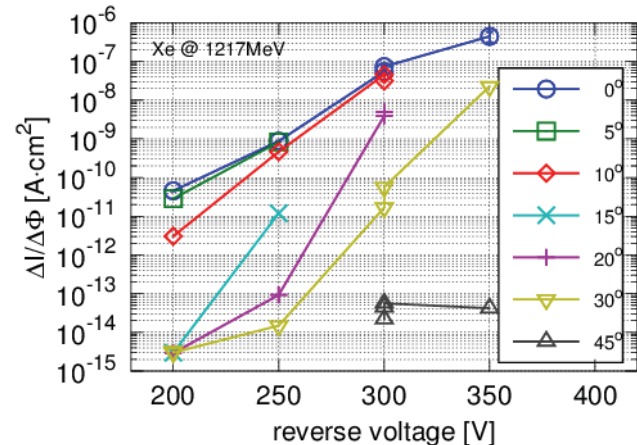


Figure 2. Degradation rates for the seven tilting configurations measured for STPSC10H065DY diodes as a function of bias voltage applied during irradiation for 1217-MeV Xe-ion exposure.

The degradation rates (leakage current/ion fluence) for an STPSC10H065DY diode at several tilting angles are presented in Figure 2 as a function of bias voltage applied during irradiation. Here it is clearly observed

that the degradation rates, obtained for a tilted configuration, are lower than that for a normal incidence beam, especially at lower bias voltages. At higher voltages, the difference becomes smaller. Nevertheless, these data illustrate the fact that the degradation rate depends strongly on the bias voltage, as reported earlier in [15].

When the bias voltage is constant and the tilting angle increases, the rate of degradation decreases and it exhibits a dramatic decrease above a certain bias-dependent angle. This effect is illustrated in Figure 3, where the evolution of leakage current for an STPSC10H065DY diode during Xe-ion exposure is given for several different tilting angles. For these data, the devices were biased at -200 V during exposure.

Here the degradation rates at angles for 15° , and above, are below the detection limit of the equipment setup. Even though the leakage current for angles between 15° and 30° seems to increase during ion exposure, the current-voltage characteristics, determined after the exposure exhibited no significant change. The effect of shadowing from the bonding wire and the package edges, were visually checked at higher tilting angles, and can be considered not to cause the observed reduction in the degradation rates.

The strong angular dependence is summarized in Figure 4, where the degradation rates are given for both SiC diode types of this work, irradiated under bias voltages from -200 V to -350 V , and at tilting angles ranging from 0° to 45° . The data show clearly the strong angular dependence of the degradation rate. The degradation response is very symmetrical for positive and negative tilting angles. At a bias voltage of -200 V ,

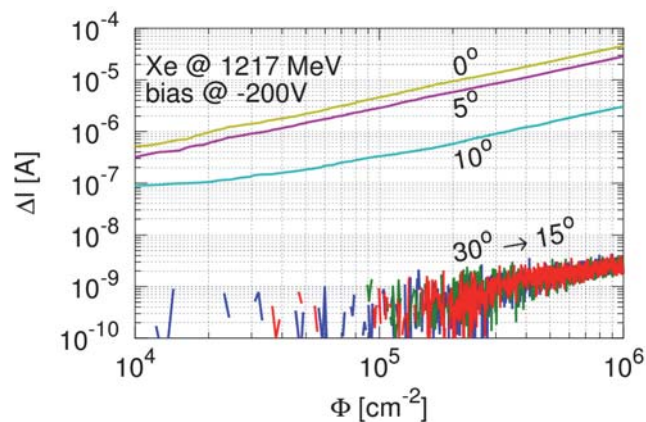


Figure 3. The change in leakage current for STPSC10H065DY diode as a function of cumulative ion fluence for 1217-MeV Xe ions at different tilting angles, obtained while the diode was biased at -200 V . All data are obtained for a single device.

the degradation rate decreases below the detection limit already at 15° of tilting, as illustrated in both Figure 3 and Figure 4. At higher bias voltage, the degradation still depends very strongly on the tilting angle, but higher tilting angles are required for reducing the degradation below the detection limit.

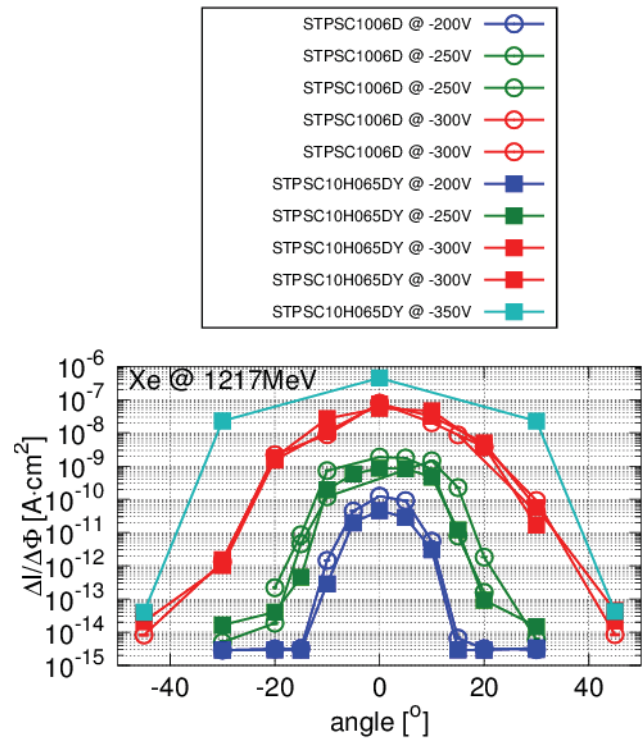


Figure 4. Degradation rate as a function of tilting angle measured by using bias voltages from -200 V to -350 V during irradiation for Xe ion exposure. Data for each bias voltage for each of the devices are taken for a single device.

The data in Figure 4 are presented as a heat map in Figure 5 that illustrates the increase in the solid angle of vulnerability for heavy-ion induced degradation for these SiC Schottky diodes. The degradation rate at -350 V bias and normal incidence is almost four orders of magnitude greater than that at -200 V bias. The degradation induced by Xe-ions in these diodes at -350 V is reduced below the detection limit at a tilting angle of approximately 45° , whereas for -200 V this occurs already at angles $\sim 15^\circ$. These kinds of data could be used in estimating the on-orbit behavior of these types of parts. However, in order to obtain more precise degradation estimates, experimental data for other ions (lighter than Xe) would be required.

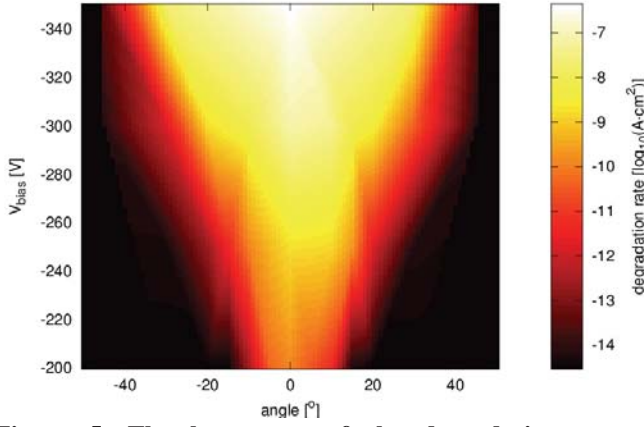


Figure 5. The heat map of the degradation rate as functions of tilting angle (abscissa) and the bias voltage (ordinate) obtained for 1217-MeV Xe-ion beam. Black color corresponds to the area, where no degradation is observed.

IV. TCAD SIMULATIONS AND DISCUSSION

In order to develop insight into the heavy-ion induced thermo-electric response in the diode structure, VICTORY DEVICE TCAD tools from Silvaco Inc. [16] have been used. In the simulations, a 3D model was used with lateral dimensions of $40\mu\text{m} \times 40\mu\text{m}$ for a normal ion strike, and $40\mu\text{m} \times 80\mu\text{m}$ for tilted ion tracks (the ion track Gaussian radius was $0.05\mu\text{m}$). The vertical depth of the 3D model was $120\mu\text{m}$, which was verified not to affect the temperature results with respect to the full depth of the actual experimental SiC substrate depth ($\sim 300\mu\text{m}$). Note that the range of the Xe ions at 1217 MeV in SiC is about $60\mu\text{m}$. For the average electron-hole pair creation energy in SiC, a value of 7.5 eV was used, which is an average between the recently published experimental values of 7.28 eV [17] and 7.8 eV [18]. For modeling the avalanche multiplication, an anisotropic impact ionization model that is especially developed and calibrated for 4H-SiC power devices [19] has been implemented in Silvaco TCAD tools, and was used in this work. Also, the thermionic emission model was activated in all simulations.

Figure 6 shows the temporal evolution of the anode current in the SiC Schottky diode simulated for Xe-ion strikes at normal incidence and at tilting angles of 3° , 5° , 10° , 15° and 30° . In the simulations, the charge, representing the ion strike, is deposited with a Gaussian time profile that has a peak at time $t = 5\text{ ps}$ and a rise time of 0.5 ps . The anode current reaches its maximum at about $t = 6.3\text{ ps}$ at all simulated tilting angles. Moreover, the charge deposited by the ion modifies the electric field within the Schottky structure. This is illustrated in Figure 7, where the simulated electric field

distribution along the center of the ion track is presented before, and at two time instants after the ion strike.

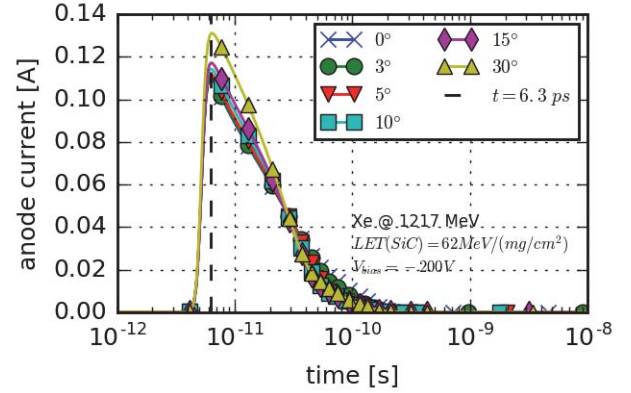


Figure 6. Temporal evolution of the anode current. Note: The data for 0° angle lies behind the other data plots. The current data for 0° and 3° are almost identical.

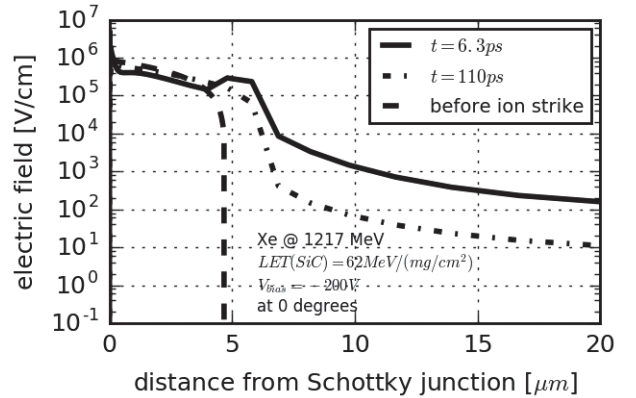


Figure 7. Electric field distribution along the center of the ion track before (blue dashed), and at time instants of 6.3 ps (solid) and 110 ps (dash-dot). These data correspond to -200 V bias voltage and normal ion incidence.

In addition, the heat power density at the ion hit location just underneath (1 nm below) the Schottky junction was determined. The heat power density is defined by the scalar product of current density and electric field, that will cause temperatures to rise in the structure via Joule heating. The results are presented in Figure 8. From these data, one can see that the heat power density exhibits two maxima at this location, one at $t \approx 6.3\text{ ps}$ and another at $t \approx 110\text{ ps}$. A close analysis of the 3D TCAD simulation results suggests that the carriers (electrons and holes) at the ion track boundaries diffuse much faster than the ones from the track core (due to high gradients at the track side boundaries), so after a while ($\sim 100\text{ ps}$) the highly-conductive path (i.e. track core) becomes even narrower, so the current

density in the core becomes even higher for a moment, due to "squeezing" of the total current into this narrower conductive path. This leads to a momentary increase of power density in the second peak. The figure also shows the lattice temperature at 5 nm below the Schottky junction. Evidently, the lattice temperature reaches its maximum when the heat power density reaches its second maximum. However, both peaks in the heat power density play a role in the total lattice temperature evolution. Because there is no significant difference in the electric field before and after the ion strike at this location, the temperature rise can be attributed to the changes in the current density.

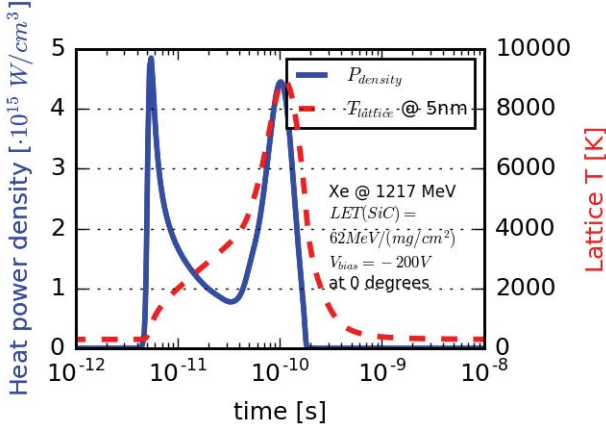


Figure 8. The heat power density (solid blue) and the SiC lattice temperature (dashed red) at the ion hit location just 5 nm below the Schottky junction for -200 V bias and normal ion incidence.

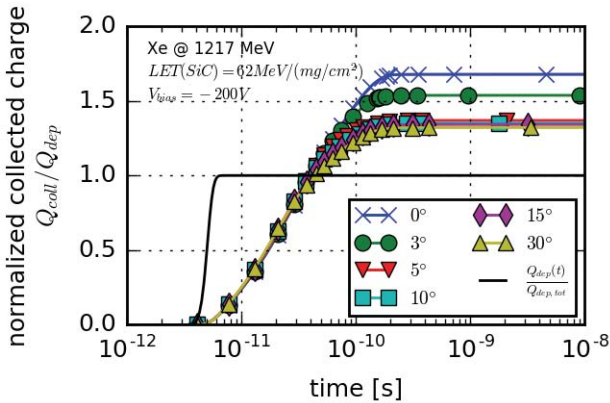


Figure 9. Temporal evolution of the charge collection in SiC Schottky structure, biased at -200V and hit by Xe-ion. The collected charge Q_{coll} is normalized with the total deposited charge Q_{dep} in the depletion region. Also, the charge deposition timing is illustrated (solid black line).

The electron-phonon coupling can be assumed to occur at timescales of $< 1ps$ [20], [21]. This means that during the Joule heating, the temperature equilibrium

between electrons and the lattice atoms is achieved faster than the timescales involved in the charge collection. Time scales for cumulative charge deposition and collection for different tilting configurations are presented in Figure 9. Figure 10 presents the corresponding simulated temporal evolution of the maximum lattice temperatures. In these simulations, -200V bias was applied to the diode corresponding to one of the experimental configurations discussed above. The results show dramatic differences in the maximum local temperatures occurring in a reverse-biased diode structure promptly after the ion strike at different ion incident angles. The simulations show that the maximum temperature is obtained at or near the Schottky contact, along the path of the incident ion. For normal incidence, the size of the volume where the temperature exceeds the melting temperature of SiC (3000K)[22] is estimated to be about 80 nm across and 100 nm along the track. The location for the maximum peak SiC lattice temperature is reached few nanometers below the Schottky junction and it is reached at about 110 ps after the ion strike.

In the simulation results given in Figure 10, already at 5° the maximum lattice temperature in the structure is much less than the values obtained at the normal incidence. Moreover, at higher tilting angles the maximum lattice temperatures are even lower. This is attributed to fact that since the current density and electric field vectors are aligned they produce higher power densities than at tilted configuration.

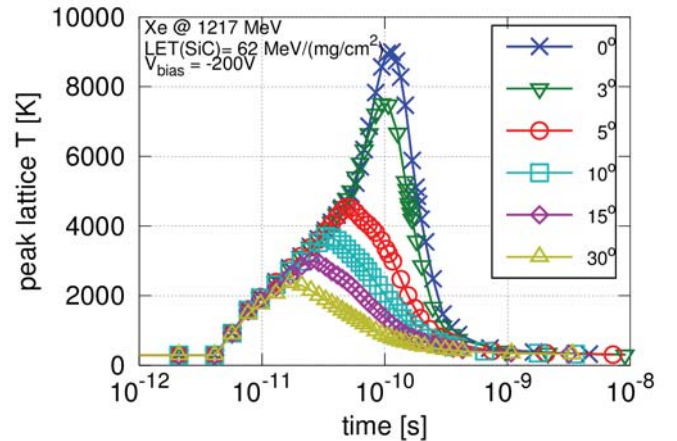


Figure 10. Temporal evolution of the maximum lattice temperatures at six different ion incident angles, simulated in SiC Schottky diode structure after 1217-MeV Xe ion strike. The average LET value of $62 MeV/(mg/cm^2)$ is used for estimating the ion's energy deposition. After [11].

For an ion strike at normal incidence, the simulated maximum lattice temperature values exceed 9000K, which is much higher than the melting temperature of

SiC. These simulation results are only qualitative, as the models used in TCAD are not verified at these high temperatures. E.g., the values for electron and hole mobilities in SiC are experimentally validated only for temperatures up to about 700 K [23]. Above this temperature the parameter values used in the simulations are based on extrapolations. Most importantly, these temperature values should be used only as a proxy indicator for the assumed damage in the lattice; i.e., when the simulations indicate that the lattice temperature approaches or exceeds the melting temperature, there is a high likelihood of damage, but the specific temperature reached is not known. Even though the temperature values obtained from TCAD simulations do not present a precise measure of the local lattice properties at very short timescales and small volumes, the simulations indicate that at normal incidence the energy dissipation is much higher than in the tilted configuration.

Hence, the maximum energy dissipation occurring at normal incidence appears to be sufficient to cause material modifications that induce higher leakage current degradation in SiC Schottky diodes under heavy-ion exposure compared to that at tilted beam angles.

The decreased energy dissipation with increasing tilting angle is consistent with the lower degradation rates. Moreover, the lower energy dissipation at higher angles can be explained by the misalignment of the heavy-ion induced charge filament and the potential gradient within the depletion region. This reduces the current density within the Schottky structure, which leads to reduced density of overall energy dissipation. This reduction in current density can also be estimated with simple geometrical analysis of the ion track. The ion's trajectory within the depletion layer is illustrated in Figure 11.

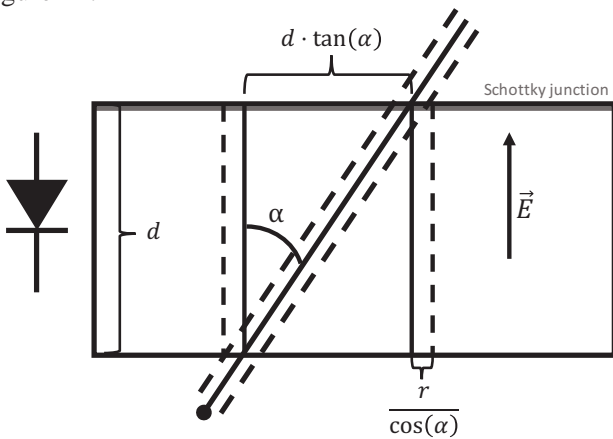


Figure 11. Illustration of the ion passing through the depletion layer at a tilting angle α .

With a similar analysis as presented in [8], we have assumed that the ion deposits its energy within a certain

radius, r , from its trajectory. Hence, the initial ion-deposited energy in the depletion region can be estimated to be within a volume of $V_{dep} = \pi \cdot r^2 \cdot d / \cos(\alpha)$, where d is the bias-dependent thickness of the depletion layer and α is the tilt angle. One should keep in mind that in reality the ion-deposited energy within the ion track is highly concentrated near the ion's trajectory [24], but for simplicity here an even energy distribution is assumed within a given track radius r .

The deposited energy is now assumed to be transformed fully into charge, not considering possible recombination or charge multiplication effects at this stage. Moreover, this charge is collected by the electric field that is normal to the surface of the diode. Now, we can consider the charge collection volume, from which the charge is collected, to increase with increasing angle. The general form for the charge collection volume can be written

$$V_{coll} = \left(\frac{\pi r^2 d}{\cos(\alpha)} + 2rd^2 \tan(\alpha) \right). \quad (1)$$

We also consider the area where the charge collection occurs at the anode. At normal incidence, the area is simply $A_0 = \pi r^2$. At tilted angles the collected charge is projected to the anode within an area of

$$A_{coll} = \left(\frac{\pi r^2}{\cos(\alpha)} + 2rd \tan(\alpha) \right) \quad (2)$$

or simply, $A_{coll} = \frac{V_{coll}}{d}$. The charge collection area basically determines the current density and therefore the power density due to Joule heating.

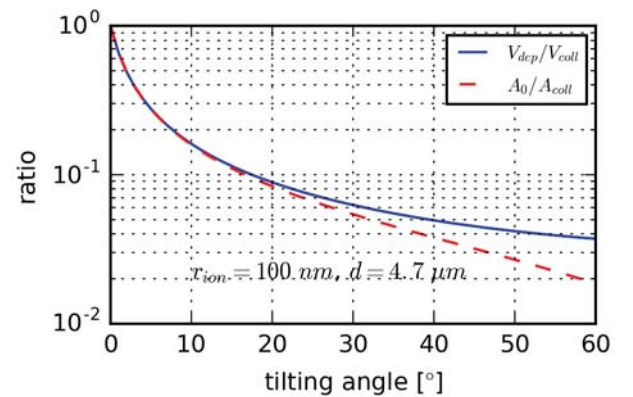


Figure 12. Ratio between the charge deposition and collection volumes (solid blue), and between the charge collection areas at normal incidence and at tilted angles (dashed red).

By looking at the ratios of both $\frac{V_{dep}}{V_{coll}}$ and $\frac{A_0}{A_{coll}}$, we can see a very clear reduction as a function of tilting angle.

This is illustrated in Figure 12. For these data, we have used values $r = 100\text{nm}$ and $d = 4.7\text{ }\mu\text{m}$. The used depletion layer thickness of $4.7\text{ }\mu\text{m}$ used in these calculations corresponds to the value for -200V bias.

From the TCAD simulation data one can obtain the collected charge, Q_{coll} , after the ion strike. By comparing this to the charge, Q_{dep} , deposited by the ion within the depletion region, the avalanche multiplication due to high electric field can be estimated.

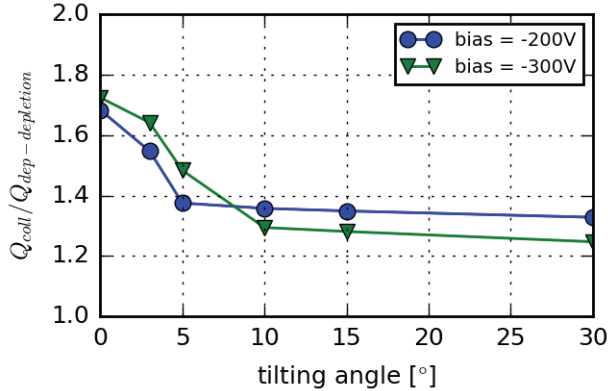


Figure 13. Ratio of the total collected charge to the total deposited charge in the depletion region for the simulated SiC Schottky diode.

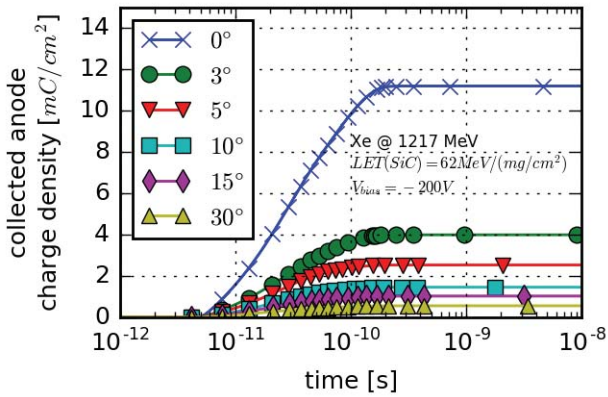


Figure 14. Temporal evolution of the collected charge density at the anode for different tilt angles and for -200V bias voltage. These data correspond to the charge collection and temperature data presented in Figure 9 and Figure 10, respectively.

The ratio between collected charge and deposited charge is presented in Figure 13 as a function of tilting angle for two biasing conditions. These data show a reduction in charge multiplication with increasing tilt angle. Over all, the charge multiplication ranges between 1.2 to 1.7 for both considered bias voltages. The difference between the charge (avalanche) multiplication at normal incidence and at tilted angles is not sufficient to alone explain the experimental observations.

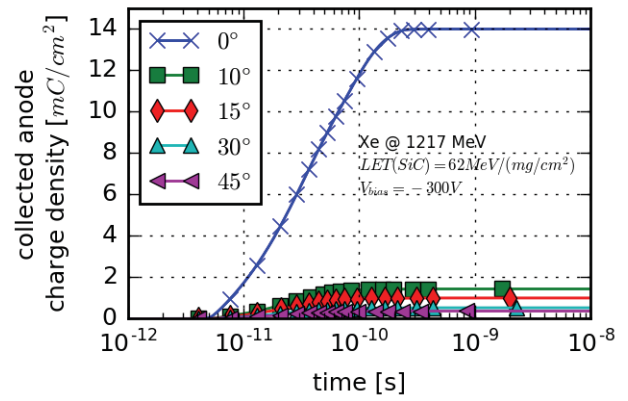


Figure 15. Temporal evolution of the collected charge density at the anode for different tilt angles and for -300V bias voltage.

However, by integrating the simulated charge that is accumulated at the anode after the ion strike, and by estimating from Eq.(2) the area where the charge is collected, we can illustrate the effect of tilting on the collected charge density. These data are presented in Figure 14 and Figure 15, where the cumulative charge collected at the anode is plotted as a function of time after the ion strike for several tilting angles and for two different bias configurations.

The simulation results in Figure 14 correspond to the charge collection and temperature data presented in Figure 9 and Figure 10, respectively. These calculations assume the collected charge is evenly distributed over the whole area given by Eq.(2). In reality, the process is more complex and the details of the charge collection are different, but this back-of-the-envelope calculation provides insight about the underlying phenomenon.

All in all, from these data, we can see a dramatic reduction in the density of collected charge at the anode (i.e., through the Schottky junction) with increasing tilt angle. This is assumed to cause the observed reduction in the degradation rates. The total cumulative charge, collected at normal incidence and at higher bias, is attributed to higher deposited charge due to thicker depletion region.

As mentioned above, the dissipated power density that governs the Joule heating, depends on the inner product of the electric field and the current density. Hence, at higher angles the reduced collected charge density will result in reduced power densities, which in turn can be assumed to cause the experimentally observed reduction in the ion-induced degradation rates in the SiC Schottky diodes.

At normal incidence, the higher power density at higher reverse bias explains also the higher degradation rates observed at higher reverse biases.

V. CONCLUSIONS

This work presents the dependence of heavy-ion induced degradation in SiC Schottky power diodes on the tilting angle. The degradation is observed to be the highest at normal incidence. Dramatic reduction in the degradation rates are observed at beam tilting angles at and above about 10° with respect to normal of the device surface, depending on the applied reverse bias. At higher reverse bias voltages, higher tilting angles are required in order to reduce the degradation below the detection limit.

TCAD simulations indicate that this observation could be explained by the differences in the prompt thermal effects within the SiC Schottky structure at different tilting angles. A thermal spike within the SiC lattice is generated by the synergetic effects of applied reverse-bias and the heavy-ion strike. When an ion traverses the Schottky diode structure at angles that are not parallel to the applied electric field, the maximum simulated lattice temperatures are reduced dramatically. Schematically this can be explained by the geometry of the ion-induced charge deposition. At higher tilting angles the induced charge is collected within a larger area compared to the area at normal incidence. Hence the power density, determined by the inner product of the electric field and current density vectors, is decreased with increasing tilting angle. This decrease in the power density is assumed to cause the experimentally observed reduction in the ion-induced degradation rates in the SiC Schottky diodes at higher tilting angles.

The results presented in this work can be used for improving the error rate and survivability estimations for SiC Schottky power devices operating in harsh radiation environments. However, more experimental data are needed for ions lighter than Xe, which was used in this work.

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