

# Beyond CMOS technologies for enabling integrating Artificial Intelligence at the Edge

Aida Todri-Sanial, Thierry Gil, Madeleine Abernot, Corentin Delacour, Stefania Carapezzi, Gabriele Boschetto, Siegfried Karg, Olivier Maher, Armin Klummp, Jamila Boudadden, et al.

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## EPoSS Annual Forum 2021 "Towards New Horizons"

### October 4 – 7, 2021

### Submission to the Call for Presentations

#### (please send to <u>contact@smart-systems-integration.org</u> before **12 July 2021**)

Proposed presentation title	Beyond CMOS technologies for enabling integrating Artificial Intelligence at the Edge		
Authors (please highlight corresponding author / speaker)	Aida Todri-Sanial, Thierry Gil, Madeleine Abernot, Corentin Delacour, Stefania Carapezzi, Gabriele Boschetto, Siegfried Karg, Olivier Maher, Armin Klummp, Jamila Boudadden, Bernabe Linares-Barranco, Maria J. Avedillo, Juan Nunez, Manuel Jimenez, J. Shamsi, Theofile Gonos, Alexandre Magueresse, Tanguy Hardelin, Ahmed Nejim, Slobodan Mijalkovic		
Abstract	(1,500 – 3,000 characters)		
	With the increase of Artificial Intelligence (AI) in everyday life, developing Al- specific hardware based on brain-inspired computing is of utmost importance for efficient, adaptative and low-power systems. Neuro-inspired computing systems emulate the human brain's neuronal functions to efficiently solve problems that are easy to humans, such as pattern recognition. In this context, the EU H2020 <u>NeurONN</u> project explores a new energy-efficient computing paradigm based on phase-computing Oscillatory Neural Networks (ONN) [1,2]. It aims to create a neurocomputing chip that can be deployed on edge devices for AI [3,4,5,6]. In this talk, a novel and alternative neuromorphic computing paradigm based on oscillating neural networks (ONN) will be presented. Energy efficient relaxation oscillators based on phase-change VO <sub>2</sub> material for oscillating neurons and tunable 2D TMD MoS <sub>2</sub> memristors for synapses are the building blocks of ONN architecture. Inspired by neural oscillations or brain waves, in ONN, the information is encoded in the phase of coupled oscillators. The talk will cover aspects from materials, devices, circuits to ONN architecture design and hardware implementation and demonstration on AI tasks. To demonstrate the ONN operation, we create a robotic application using two ONNs serially (ONN 1 feeds ONN 2), configured for pattern recognition to perform obstacle avoidance. We use a robot (see Figure 1) equipped in the front with eight infrared proximity sensors.		
	NewORN		
	Network (ONN) to avoid obstacles.		
	References: [1] E. M. Izhikevich, Computing with Oscillators, Neural Networks, 2000. [2] F. C. Hoppensteadt and E. M. Izhikevich, Pattern recognition via synchronization in phase-locked loop neural networks, in <i>IEEE Transactions on Neural Networks</i> , 2000.		



[3] A. Todri-Sanial, S. Carapezzi, C. Delacour, M. Abernot, E. Karachristou, et al
Efficient Neuromorphic Computing. European Forum for Electronic Components
[4] E. Corti, A. Khanna, K. Niang, J. Robertson, K. Moselund, B. Gotsmann, S. Datta, S. Karg, Time-Delay Encoded Image Recognition in a Network of Resistively
[5] E. Corti, B. Gotsmann, K. Moselund, A. Ionescu, J. Robertson, S. Karg, Scaled Resistively Coupled VO2 Oscillators for Neuromorphic Computing, Solid State
Electronics, 2020. [6] A. Todri-Sanial, S. Carapezzi, C. Delacour, M. Abernot, T. Gil, et al How Frequency Injection Locking Can Train Oscillatory Neural Networks to Compute in Phase. 2021

Contact details of	Title	Dr	
corresponding author /	First name	Aida	
speaker	Last name	Todri—Sanial	
	Organisation	CNRS	
	Department	LIRMM, Microelectronics Department	
	Position	Director of Research	
	Address	161 rue Ada, Montpellier, France 34095	
	Email	aida.todri@lirmm.fr	
	Phone	+33 4 67 14 97 53	
Short CV	Aida Todri-Sanial received the B.S. degree in electrical engineering from Bradley University, IL in 2001, M.S. degree in electrical engineering from Long Beach State University, CA, in 2003 and a Ph.D. degree in electrical and computer engineering from the University of California, Santa Barbara, in 2009. She is currently a Director of Research for the French National Council of Scientific Research (CNRS) attached to Laboratoire d'Informatique de Robotique et de Microélectronique de Montpellier (LIRMM). Dr. Todri-Sanial was a visiting fellow at the Cambridge Graphene Center and Wolfson College at the University of Cambridge, UK during 2016-2017. Previously, she was an R&D Engineer for Fermi National Accelerator Laboratory, IL. She has also held visiting research positions at Mentor Graphics, Cadence Design Systems, STMicroelectronics and IBM TJ Watson Research Center. Her research interests focus on emerging technologies and novel computing paradigms such as neuromorphic and quantum computing.		
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